SN54LV123A, SN74LV123A DUAL RETRIGGERABLE MONOSTABLE MULTIVIBRATORS

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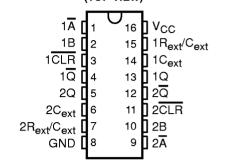
- EPIC™ (Enhanced-Performance Implanted CMOS) Process
- Typical V_{OLP} (Output Ground Bounce)
 < 0.8 V at V_{CC}, T_A = 25°C
- Typical V_{OHV} (Output V_{OH} Undershoot)
 > 2 V at V_{CC}, T_A = 25°C
- Edge Triggered From Active-High or Active-Low Gated Logic Inputs
- Retriggerable for Very Long Output Pulses, up to 100% Duty Cycle
- Overriding Clear Terminates Output Pulse
- Package Options Include Plastic Small-Outline (D, NS), Shrink Small-Outline (DB), Thin Very Small-Outline (DGV), and Thin Shrink Small-Outline (PW) Packages, Ceramic Flat (W) Packages, Chip Carriers (FK), and DIPs (J)

description

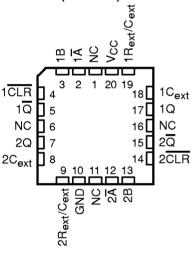
The 'LV123A devices are dual retriggerable monostable multivibrators designed for 2-V to $5.5\text{-V}\ \text{V}_{CC}$ operation.

These edge-triggered multivibrators feature output pulse-duration control by three methods. In the first method, the \overline{A} input is low and the B input goes high. In the second method, the B input is high and the \overline{A} input goes low. In the third method, the \overline{A} input is low, the B input is high, and the clear (\overline{CLR}) input goes high.

SN54LV123A . . . J OR W PACKAGE SN74LV123A . . . D, DB, DGV, NS, OR PW PACKAGE (TOP VIEW)



SN54LV123A . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

The basic pulse duration is programmed by selecting external resistance and capacitance values. The external timing capacitor must be connected between C_{ext} and R_{ext}/C_{ext} (positive) and an external resistor connected between R_{ext}/C_{ext} and V_{CC} . To obtain variable pulse durations, connect an external variable resistance between R_{ext}/C_{ext} and V_{CC} .

Once triggered, the basic pulse duration can be extended by retriggering the gated low-level-active (\overline{A}) or high-level-active (B) input. Pulse duration can be reduced by taking \overline{CLR} low. Figure 1 illustrates pulse control by retriggering the inputs and early clearing.

The SN54LV123A is characterized for operation over the full military temperature range of –55°C to 125°C. The SN74LV123A is characterized for operation from –40°C to 85°C.



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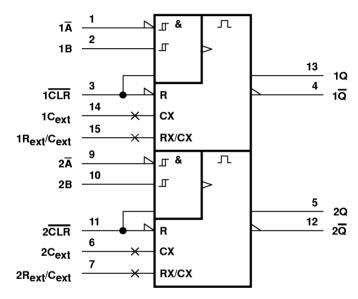


FUNCTION TABLE

ı	NPUTS	ı	OUTPUTS				
CLR	Ā	В	Q	Ω			
L	Х	Х	L	Ι			
×	Н	X	∟†	н†			
×	Χ	L	L†	H [†]			
н	L	\uparrow	л	ъ			
н	1	Н	л	Т			
1	L	Н	л	ъ			

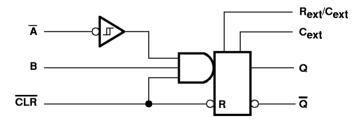
†These outputs are based on the assumption that the indicated steady-state conditions at the A and B inputs have been set up long enough to complete any pulse started before the setup.

logic symbol‡



[‡]This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, DB, DGV, J, NS, PW, and W packages.

logic diagram, each multivibrator (positive logic)



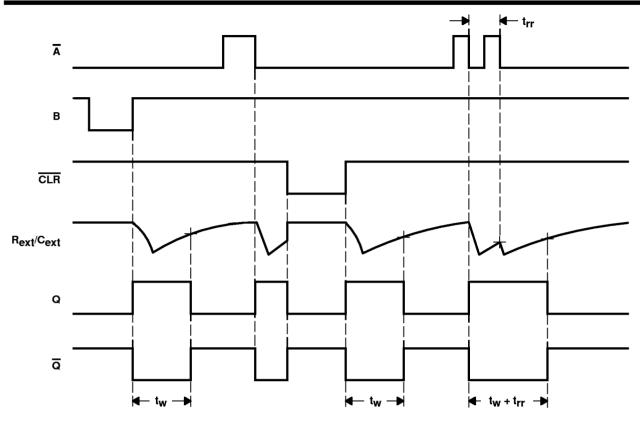


Figure 1. Input and Output Timing

absolute maximum ratings over operating free-air temperature (unless otherwise noted)†

$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Supply voltage range, V _{CC}	–0.5 V to 7 V
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Input voltage range, V _I (see Note 1)	–0.5 V to 7 V
$\begin{array}{cccc} \text{Output clamp current, } I_{\text{OK}} \left(V_{\text{O}} < 0 \text{ or } V_{\text{O}} > V_{\text{CC}} \right) & \pm 50 \text{ mA} \\ \text{Continuous output current, } I_{\text{O}} \left(V_{\text{O}} = 0 \text{ to } V_{\text{CC}} \right) & \pm 25 \text{ mA} \\ \text{Continuous current through } V_{\text{CC}} \text{ or GND} & \pm 50 \text{ mA} \\ \text{Package thermal impedance, } \theta_{\text{JA}} \left(\text{see Note 3} \right) \text{: D package} & 113^{\circ}\text{C/W} \\ & \text{DB package} & 131^{\circ}\text{C/W} \\ & \text{DGV package} & 180^{\circ}\text{C/W} \\ & \text{NS package} & 111^{\circ}\text{C/W} \\ & \text{PW package} & 149^{\circ}\text{C/W} \\ \end{array}$	Output voltage range, VO (see Notes 1 and 2)	
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	Input clamp current, $I_{ K }(V_{ C } < 0)$	
$ \begin{array}{c} \text{Continuous current through V_{CC} or GND} & \pm 50 \text{ mA} \\ \text{Package thermal impedance, θ_{JA} (see Note 3): D package} & 113^{\circ}\text{C/W} \\ \text{DB package} & 131^{\circ}\text{C/W} \\ \text{DGV package} & 180^{\circ}\text{C/W} \\ \text{NS package} & 111^{\circ}\text{C/W} \\ \text{PW package} & 149^{\circ}\text{C/W} \\ \end{array} $	Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC}	y) ±50 mA
Package thermal impedance, θ _{JA} (see Note 3): D package113°C/WDB package131°C/WDGV package180°C/WNS package111°C/WPW package149°C/W	Continuous output current, $I_O(V_O = 0 \text{ to } V_{CC})$	±25 mA
DB package 131°C/W DGV package 180°C/W NS package 111°C/W PW package 149°C/W	Continuous current through V _{CC} or GND	±50 mA
DGV package 180°C/W NS package 111°C/W PW package 149°C/W	Package thermal impedance, θ_{JA} (see Note 3):	D package 113°C/W
NS package 111°C/W PW package 149°C/W		DB package
PW package		DGV package 180°C/W
· · ·		NS package
Storage temperature range, T _{stg} 65°C to 150°C		PW package 149°C/W
	Storage temperature range, T _{stg}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

- 2. This value is limited to 7 V maximum.
- 3. The package thermal impedance is calculated in accordance with JESD 51.



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recommended operating conditions (see Note 4)

			SN54L	V123A	SN74I	-V123A	UNIT
			MIN	MAX	MIN	MAX	UNIT
Vcc	Supply voltage		2	5.5	2	5.5	V
		V _{CC} = 2 V	1.5		1.5		
V	High level innut values	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	V _{CC} ×0.7	7	$V_{CC} \times 0$.	7	V
V_{IH}	High-level input voltage	$V_{CC} = 3 \text{ V to } 3.6 \text{ V}$	V _{CC} ×0.7	7	$V_{CC} \times 0$.	7	V
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	V _{CC} ×0.7	7	$V_{CC} \times 0$.	7	
		V _{CC} = 2 V		0.5		0.5	
V	Levelovel input veltage	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$,	√ _{CC} × 0.3		V _{CC} ×0.3	V
V_{IL}	Low-level input voltage	$V_{CC} = 3 \text{ V to } 3.6 \text{ V}$,	√ _{CC} × 0.3		V _{CC} × 0.3	V
		V _{CC} = 4.5 V to 5.5 V	,	√ _{CC} × 0.3		V _{CC} ×0.3	
VI	Input voltage		0	5.5	0	5.5	V
٧o	Output voltage		0	Vcc	0	Vcc	V
		V _{CC} = 2 V		– 50		– 50	μΑ
1=	High level output ourrent	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		-2		-2	
ЮН	High-level output current	$V_{CC} = 3 \text{ V to } 3.6 \text{ V}$		- 6		4	mA
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		-12		-12	
		V _{CC} = 2 V		50		50	μΑ
la.	Low-level output current	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2		2	
lol	Low-level output current	$V_{CC} = 3 \text{ V to } 3.6 \text{ V}$		6		6	mA
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		12		12	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	0	200	0	200	
$\Delta t / \Delta v$	Input transition rise or fall rate	$V_{CC} = 3 \text{ V to } 3.6 \text{ V}$	0	100	0	100	ns/V
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	0	20	0	20	
TA	Operating free-air temperature		– 55	125	-4 0	85	°C

NOTE 4: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.



PRODUCT PREVIEW

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	ARAMETER	TEST CONDITIONS		SN	54LV123	3A	SNZ	74LV123	BA	UNIT
	ARAMETER	TEST CONDITIONS	Vcc	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
		IOH = -50 μA	2 V to 5.5 V	V _{CC} -0.	1		V _{CC} -0.	1		
 		I _{OH} = -2 mA	2.3 V	2			2			٧
VOH		I _{OH} = -6 mA	3 V	2.48			2.48			V
		I _{OH} = -12 mA	4.5 V	3.8			3.8			
		I _{OL} = 50 μA	2 V to 5.5 V			0.1			0.1	
		I _{OL} = 2 mA	2.3 V			0.4			0.4	٧
VOL		I _{OL} = 6 mA	3 V			0.44			0.44	V
		I _{OL} = 12 mA	4.5 V			0.55			0.55	
	R _{ext} /C _{ext} †	V _I = V _{CC} or GND	5.5 V			±1			±1	
Ц		V. Von Grend	0 V			±1			±1	μΑ
	A, B, and CLR	V _I = V _{CC} or GND	5.5 V			±1			±1	
Icc	Quiescent	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			40			40	μΑ
			2.7 V							
Icc	Active state (per circuit)	$V_I = V_{CC}$ or GND, $R_{ext}/C_{ext} = 0.5 V_{CC}$	3.6 V							μΑ
	(per circuit)	Trext Sext = 0.5 VCC	5.5 V							
l _{off}		V_{I} or $V_{O} = 0$ to 5.5 V	0 V						5	μΑ
<u> </u>		V. Vocar CND	3.3 V							~E
Ci		V _I = V _{CC} or GND	5 V							pF

[†]This test is performed with the terminal in the off-state condition.

timing requirements over recommended operating free-air temperature range, V_{CC} = 2.5 V \pm 0.2 V (unless otherwise noted) (see Figures 1 and 2)

			TEST CO	ONDITIONS	T,	T _A = 25°C			/123A	SN74L\	/123A	UNIT
			TEST CONDITIONS -		MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
	Pulse	CLR										
t _w	duration	A or B trigger										ns
Ţ.	D. J		D . 110	$C_{ext} = 100 pF$								ns
t _{rr}	t _{rr} Pulse retri	gger time	$R_{ext} = 1 k\Omega$	$C_{ext} = 0.01 \mu F$								μs

timing requirements over recommended operating free-air temperature range, V_{CC} = 3.3 V \pm 0.3 V (unless otherwise noted) (see Figures 1 and 2)

			TEST CONDITIONS		T _A = 25°C			SN54LV123A		SN74LV123A		UNIT
	<u>, </u>		TEST CONDITIONS		MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
T.	Pulse	CLR										nc
t _w	duration	A or B trigger										ns
	Dulaa watsia	4:	D . 1k0	C _{ext} = 100 pF								ns
trr	Pulse retrig	ger time	$R_{\text{ext}} = 1 \text{ k}\Omega$ $C_{\text{ext}} = 0.01 \mu\text{F}$									μs

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timing requirements over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figures 1 and 2)

			TEST CONDITIONS		T _A = 25°C			SN54LV123A		SN74LV123A		UNIT
					MIN	TYP	MAX	MIN	MAX	MIN	MAX	ONIT
t _W	Pulse duration	CLR Ā or B trigger										ns
	Dulaa vateia		P := 1 k0	C _{ext} = 100 pF								ns
^T rr	Pulse retrig	ger ume	$R_{\text{ext}} = 1 \text{ k}\Omega$ $C_{\text{ext}} = 0.01 \mu\text{F}$									μs

switching characteristics over recommended operating free-air temperature range, V_{CC} = 2.5 V \pm 0.2 V (unless otherwise noted) (see Figure 2)

				-	T _A = 25°C			V400A	CNZ411	/100A	
PARAMETER	FROM	TO (OUTPUT)	TEST				SN54L		SN74L\		UNIT
	(INPUT)	(OUTPUT)	CONDITIONS	MIN	TYP	MAX	MIN	MAX	MIN	MAX	
^t PLH*	A or B	Q or $\overline{\mathbb{Q}}$									
tPHL*	7016	4014									
^t PLH*	CLR	Q or $\overline{\mathbb{Q}}$	C _L = 15 pF								ns
^t PHL*	CLR	9679									113
^t PLH*	CLR trigger	Q or $\overline{\mathbb{Q}}$									
tPHL*	CLR (rigger	QorQ									
^t PLH	A or B	Q or $\overline{\mathbb{Q}}$									
^t PHL	AUID	QorQ									
^t PLH	CLR	Q or $\overline{\mathbb{Q}}$	C _L = 50 pF								ns
^t PHL	CLR	QorQ	OL = 30 pi								115
^t PLH	CLR trigger	Q or $\overline{\mathbb{Q}}$									
^t PHL	CLN (rigger	2012									
			$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 28 \text{ pF},$ $R_{\text{ext}} = 2 \text{ k}\Omega$								ns
_{tw} †		Q or $\overline{\mathbb{Q}}$	$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 0.01 \mu\text{F},$ $R_{\text{ext}} = 10 k\Omega$								μs
			$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 0.1 \mu\text{F},$ $R_{\text{ext}} = 10 k\Omega$								ms
∆t _w ‡											%

^{*} On products compliant to MIL-PRF-38535, this parameter is not production tested.

[†] t_W = Duration of pulse at Q and \overline{Q} outputs ‡ Δt_W = Output pulse duration variation (Q and \overline{Q}) between circuits in same package

PRODUCT PREVIEW

switching characteristics over recommended operating free-air temperature range, V_{CC} = 3.3 V \pm 0.3 V (unless otherwise noted) (see Figure 2)

PARAMETER	FROM	то	TEST	T,	Վ = 25°C	;	SN54L	.V123A	SN74L	V123A	UNIT
PARAMETER	(INPUT)	(OUTPUT)	CONDITIONS	MIN	TYP	MAX	MIN	MAX	MIN	MAX	ONIT
tPLH*	A or B	Q or Q									
tPHL*	A or B	Q or Q									
^t PLH*	CLR	Q or Q	C _L = 15 pF								ns
tPHL*	CLR	ν ο ο									113
t _{PLH} *	CLR trigger	Q or $\overline{\mathbb{Q}}$									
tPHL*	OLN (ligger	Q 01 Q									
t _{PLH}	A or B	Q or $\overline{\mathbb{Q}}$									
t _{PHL}	AOIB	Q 01 Q									
t _{PLH}	CLR	Q or $\overline{\mathbb{Q}}$	C _L = 50 pF								ns
t _{PHL}	OLN	Q 01 Q	OL = 00 β.								1.0
tPLH	CLR trigger	Q or $\overline{\mathbb{Q}}$									
tPHL	OLIT tilgger	40.4									
			$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 28 \text{ pF},$ $R_{\text{ext}} = 2 \text{ k}\Omega$								ns
_{tw} †		Q or Q	$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 0.01 \mu\text{F},$ $R_{\text{ext}} = 10 k\Omega$								μs
			C_L = 50 pF, C_{ext} = 0.1 μ F, R_{ext} = 10 $k\Omega$								ms
∆t _W ‡											%

^{*} On products compliant to MIL-PRF-38535, this parameter is not production tested.

 $[\]dagger t_{W} = Duration of pulse at Q and <math>\overline{Q}$ outputs

 $[\]ddagger \Delta t_W = \text{Output pulse duration variation (Q and } \overline{Q} \text{)}$ between circuits in same package

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switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figure 2)

DADAMETED	FROM	то	TEST	Т,	Վ = 25 °C	;	SN54L	.V123A	SN74L	V123A	UNIT
PARAMETER	(INPUT)	(OUTPUT)	CONDITIONS	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
t _{PLH} *	A or B	Q or Q									
tPHL*	AUB	Q 61 Q									
^t PLH*	CLR	Q or $\overline{\mathbb{Q}}$	C _L = 15 pF								ns
tPHL*	OLIT	4 61 4									
t _{PLH} *	CLR trigger	Q or $\overline{\mathbb{Q}}$									
t _{PHL} *											
t _{PLH}	A or B	Q or $\overline{\mathbb{Q}}$									
tPHL											
tPLH	CLR	Q or $\overline{\mathbb{Q}}$	C _L = 50 pF								ns
tPHL											
tPLH	CLR trigger	Q or $\overline{\mathbb{Q}}$									
tPHL			0 50 5								
			$C_L = 50 \text{ pF},$ $C_{ext} = 28 \text{ pF},$ $R_{ext} = 2 \text{ k}\Omega$								ns
_{tw} †		Q or $\overline{\overline{Q}}$	$C_L = 50 \text{ pF},$ $C_{\text{ext}} = 0.01 \mu\text{F},$ $R_{\text{ext}} = 10 k\Omega$								μs
			C_L = 50 pF, C_{ext} = 0.1 μ F, R_{ext} = 10 $k\Omega$								ms
∆t _w ‡											%

^{*} On products compliant to MIL-PRF-38535, this parameter is not production tested.

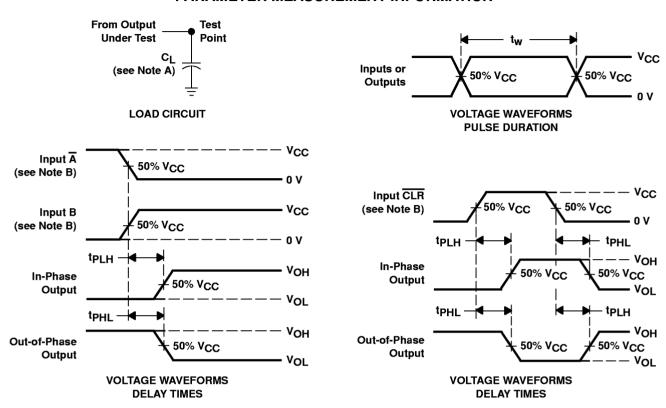
operating characteristics, T_A = 25°C

	PARAMETER	TEST CO	NDITIONS	Vcc	TYP	UNIT
<u> </u>	Dawe dissination conscitance	C. E0.5E	f 10 MU-	3.3 V		~F
Cpd	Power dissipation capacitance	$C_L = 50 pF$,	f = 10 MHz	5 V		pΕ

 $[\]dagger t_{W}$ = Duration of pulse at Q and \overline{Q} outputs

 $[\]ddagger \Delta t_W = \text{Output pulse duration variation (Q and } \overline{Q})$ between circuits in same package

PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

- B. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \Omega$, $t_f = 3 \text{ ns}$, $t_f = 3 \text{ ns}$.
- C. The outputs are measured one at a time with one input transition per measurement.

Figure 2. Load Circuit and Voltage Waveforms

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