



93L24 5-Bit Comparator

General Description

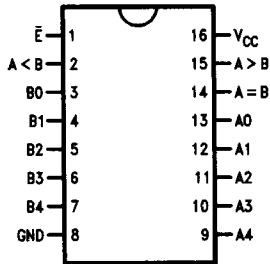
The 93L24 expandable comparator provides comparison between two 5-bit words and gives three outputs—"less than", "greater than" and "equal to". A HIGH on the active LOW Enable input forces all three outputs LOW.

Features

- Three separate outputs: A < B, A > B, A = B
- Easily expandable
- Active low enable input

Connection Diagram

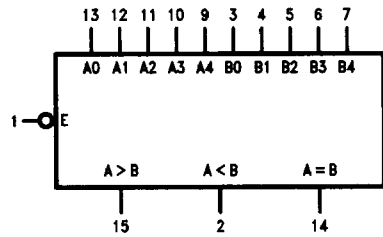
Dual-In-Line Package



TL/F/10199-1

Order Number 93L24DMQB or 93L24FMQB
See NS Package Number J16A or W16A

Logic Symbol



V_{CC} = Pin 16
GND = Pin 8

TL/F/10199-2

Truth Table

Pin Names	Description
\bar{E}	Enable Input (Active LOW)
A0–A4	Word A Parallel Inputs
B0–B4	Word B Parallel Inputs
A < B	A Less than B Output (Active HIGH)
A > B	A Greater than B Output (Active HIGH)
A = B	A Equal to B Output (Active HIGH)

Inputs			Outputs		
\bar{E}	A _n	B _n	A < B	A > B	A = B
H	X	X	L	L	L
L	Word A = Word B		L	L	H
L	Word A > Word B		L	H	L
L	Word B < Word A		H	L	L

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
MIL	-55°C to +125°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	93L24 (MIL)			Units
		Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.7	V
I _{OH}	High Level Output Current			-400	μA
I _{OL}	Low Level Output Current			4.8	mA
T _A	Free Air Operating Temperature	-55		125	°C

Electrical Characteristics over recommended operating free air temperature (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = -10 mA			-1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max, V _{IL} = Max, V _{IH} = Min	2.4			V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max, V _{IH} = Min, V _{IL} = Max			0.3	V
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V			1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V			40	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.3V			-0.8	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	-2.5		-25	mA
I _{CC}	Supply Current	V _{CC} = Max			21	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

$V_{CC} = +5.0V$, $T_A = +25^\circ C$ (See Section 1 for test waveforms and output load)

Symbol	Parameter	$C_L = 15 \text{ pF}$		Units
		Min	Max	
t_{PLH} t_{PHL}	Propagation Delay \bar{E} to $A=B$; \bar{E} to $A<B$, $A>B$		32 35	ns
t_{PLH} t_{PHL}	Propagation Delay A_n to $A>B$; B_n to $A>B$		54 75	ns
t_{PLH} t_{PHL}	Propagation Delay A_n to $A<B$; B_n to $A<B$		70 77	ns
t_{PLH} t_{PHL}	Propagation Delay A_n or B_n to $A=B$		100 102	ns

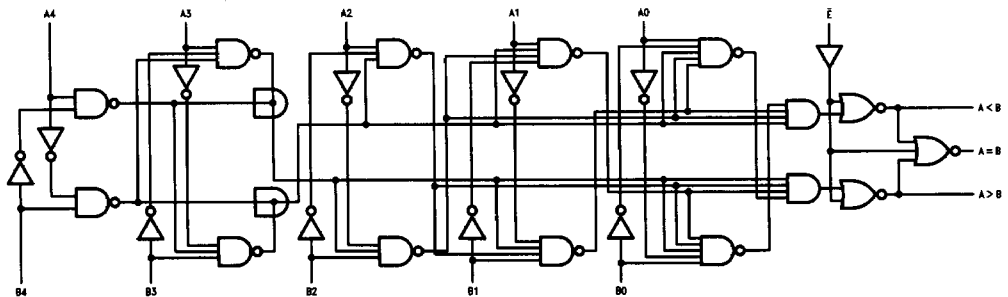
Functional Description

The 93L24 5-bit comparators use combinational circuitry to directly generate "A greater than B" and "A less than B" outputs. As evident from the logic diagram, these outputs are generated in only three gate delays. The "A equals B" output is generated in one additional gate delay by decoding the "A neither less than nor greater than B" condition with a NOR gate. All three outputs are activated by the active LOW Enable Input (\bar{E}).

Tying the $A>B$ output from one device into an A input on another device and the $A<B$ output into the corresponding B input permits easy expansion.

The A_4 and B_4 inputs are the most significant inputs and A_0 , B_0 the least significant. Thus if A_4 is HIGH and B_4 is LOW, the $A>B$ output will be HIGH regardless of all other inputs except \bar{E} .

Logic Diagram



TL/F/10199-3