FAIRCHILD SEMICONDUCTOR™

March 1998

74F521
8-Bit Identity Comparator

General Description

The 'F521 is an expandable 8-bit comparator. It compares two words of up to eight bits each and provides a LOW output when the two words match bit for bit. The expansion input $\overline{I}_{A=B}$ also serves as an active LOW enable input.

Features

- Compares two 8-bit words in 6.5 ns typ
- Expandable to any word length
- 20-pin package

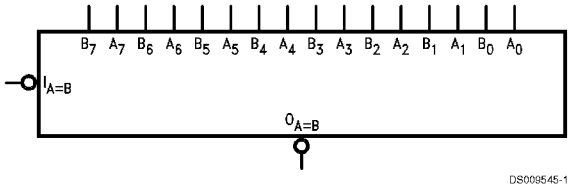
Ordering Code:

Commercial	Military	Package Number	Package Description
74F521PC		N20A	20-Lead (0.300" Wide) Molded Dual-In-Line
	54F521DM (Note 2)	J20A	20-Lead Ceramic Dual-In-Line
74F521SC (Note 1)		M20B	20-Lead (0.300" Wide) Molded Small Outline, JEDEC
74F521SJ (Note 1)		M20D	20-Lead (0.300" Wide) Molded Small Outline, EIAJ
74F521MSA (Note 1)		MSA20	20-Lead Molded Shrink Small Outline, EIAJ type II

Note 1: Devices also available in 13" reel. Use suffix = SCX, SJX and MSAX.

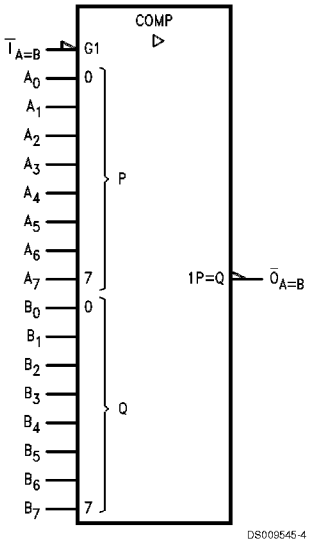
Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB.

Logic Symbols



DS009545-1

IEEE/IEC



DS009545-4

Unit Loading/Fan Out

Pin Names	Description	54F/74F	
		U.L. HIGH/LOW	Input I_{IH}/I_{IL} Output I_{OH}/I_{OL}
A_0–A_7	Word A Inputs	1.0/1.0	20 µA/–0.6 mA
B_0–B_7	Word B Inputs	1.0/1.0	20 µA/–0.6 mA
$\overline{I}_{A=B}$	Expansion or Enable Input (Active LOW)	1.0/1.0	20 µA/–0.6 mA
$\overline{O}_{A=B}$	Identity Output (Active LOW)	50/33.3	–1 mA/20 mA

Truth Table

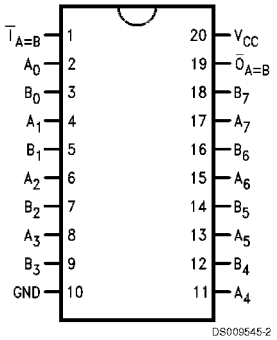
Inputs		Output
$\overline{I}_{A=B}$	A, B	$\overline{O}_{A=B}$
L	A = B (Note 3)	L
L	A ≠ B	H
H	A = B (Note 3)	H
H	A ≠ B	H

H = HIGH Voltage Level
L = LOW Voltage Level

Note 3: $A_0 = B_0$, $A_1 = B_1$, $A_2 = B_2$, etc.

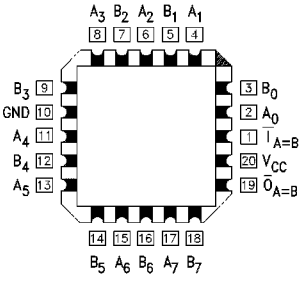
Connection Diagrams

Pin Assignment for DIP, SOIC, SSOP and Flatpak



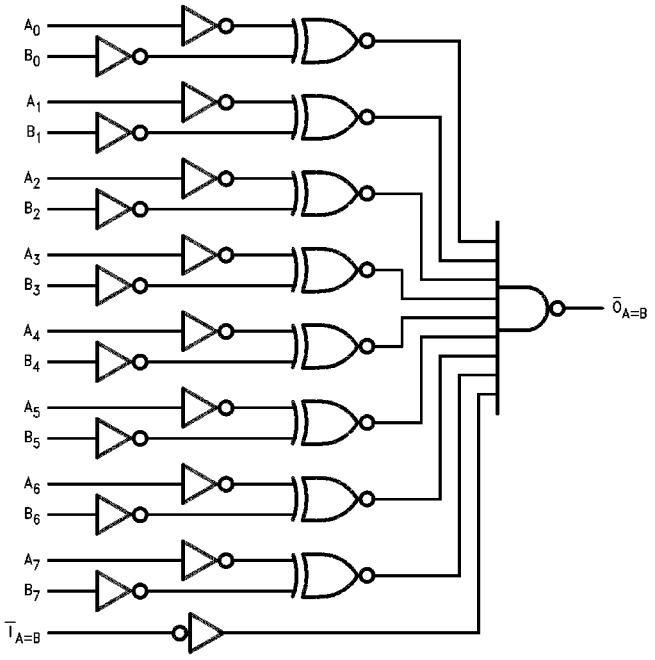
DS009545-2

Pin Assignment for LCC



DS009545-3

Logic Diagram



DS009545-5

Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 4)

Storage Temperature	−65°C to +150°C
Ambient Temperature under Bias	−55°C to +125°C
Junction Temperature under Bias	−55°C to +175°C
Plastic	−55°C to +150°C
V_{CC} Pin Potential to Ground Pin	−0.5V to +7.0V
Input Voltage (Note 5)	−0.5V to +7.0V
Input Current (Note 5)	−30 mA to +5.0 mA
Voltage Applied to Output in HIGH State (with V_{CC} = 0V)	
Standard Output	−0.5V to V_{CC}
3-STATE Output	−0.5V to +5.5V
Current Applied to Output in LOW State (Max)	twice the rated I_{OL} (mA)

Recommended Operating Conditions

Free Air Ambient Temperature	
Military	−55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

Note 4: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 5: Either voltage limit or current limit is sufficient to protect inputs.

DC Electrical Characteristics

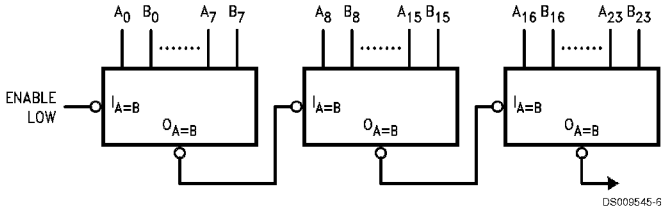
Symbol	Parameter		54F/74F Min	54F/74F Typ	54F/74F Max	Units	V_{CC}	Conditions
V_{IH}	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal
V_{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal
V_{CD}	Input Clamp Diode Voltage				−1.2	V	Min	I_{IN} = −18 mA
V_{OH}	Output HIGH Voltage	54F 10% V_{CC}	2.5			V	Min	I_{OH} = −1 mA
		74F 10% V_{CC}	2.5					I_{OH} = −1 mA
		74F 5% V_{CC}	2.7					I_{OH} = −1 mA
V_{OL}	Output LOW Voltage	54F 10% V_{CC}			0.5	V	Min	I_{OL} = 20 mA
		74F 10% V_{CC}			0.5			I_{OL} = 20 mA
I_{IH}	Input HIGH Current	54F			20.0	µA	Max	V_{IN} = 2.7V
		74F			5.0			
I_{BVI}	Input HIGH Current Breakdown Test	54F			100	µA	Max	V_{IN} = 7.0V
		74F			7.0			
I_{CEX}	Output HIGH Leakage Current	54F			250	µA	Max	V_{OUT} = V_{CC}
		74F			50			
V_{ID}	Input Leakage Test	74F	4.75			V	0.0	I_{ID} = 1.9 µA All Other Pins Grounded
I_{OD}	Output Leakage Circuit Current	74F			3.75	µA	0.0	V_{IOD} = 150 mV All Other Pins Grounded
I_{IL}	Input LOW Current				−0.6	mA	Max	V_{IN} = 0.5V
I_{OS}	Output Short-Circuit Current		−60		−150	mA	Max	V_{OUT} = 0V
I_{CCH}	Power Supply Current			21	32	mA	Max	V_O = HIGH

AC Electrical Characteristics

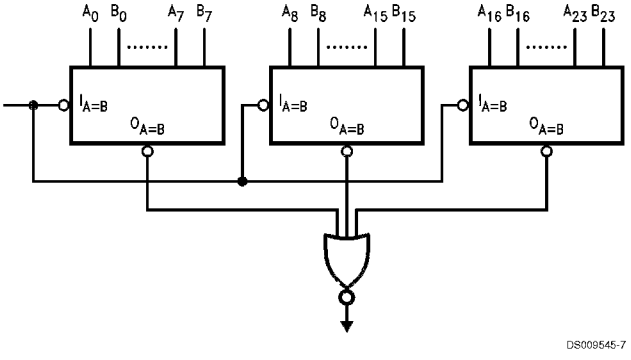
Symbol	Parameter	74F T_A = +25°C V_{CC} = +5.0V C_L = 50 pF			54F T_A, V_{CC} = Mil C_L = 50 pF		74F T_A, V_{CC} = Com C_L = 50 pF		Units
		Min	Typ	Max	Min	Max	Min	Max	
t_{PLH}	Propagation Delay	3.0	7.0	10.0	3.0	14.0	3.0	11.0	ns
t_{PHL}	A_n or B_n to $\overline{O}_{A=B}$	4.5	7.0	10.0	4.0	15.0	4.0	11.0	
t_{PLH}	Propagation Delay	3.0	5.0	6.5	3.0	8.5	3.0	7.5	ns
t_{PHL}	$\overline{I}_{A=B}$ to $\overline{O}_{A=B}$	3.5	6.5	9.0	3.5	13.5	3.5	10.0	

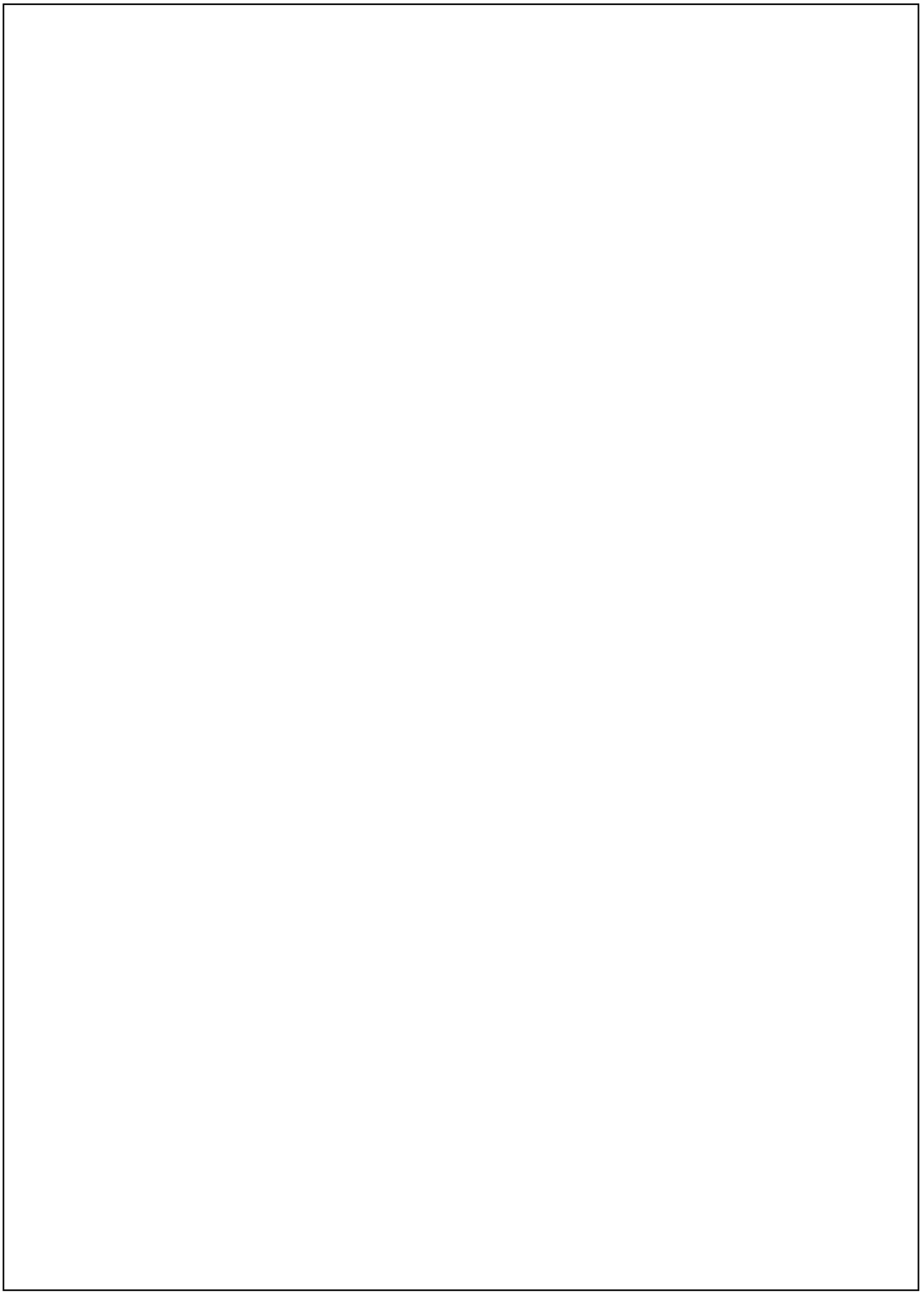
Applications

Ripple Expansion



Parallel Expansion





Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

74F 521 S C X

Temperature Range Family
- 74F = Commercial
- 54F = Military

Device Type

Package Code
- P = Plastic DIP
- D = Ceramic DIP
- F = Flatpak
- S = Small Outline SOIC JEDEC
- SJ = Small Outline SOIC EIAJ
- MSA = Shrink Small Outline (EIAJ SSOP)

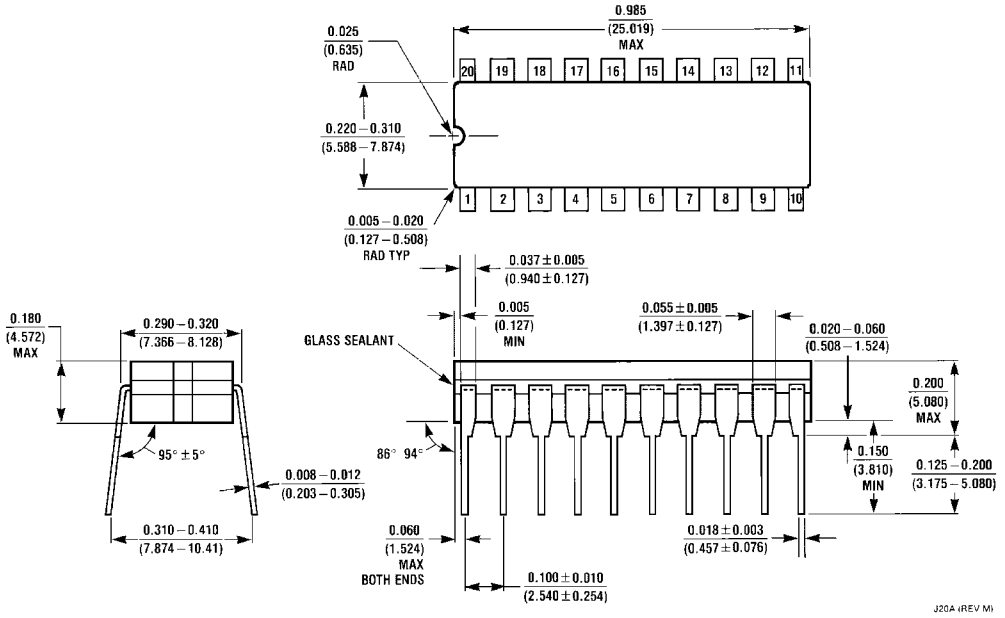
Special Variations
- QB = Military grade device with environmental and burn-in processing
- X = Devices shipped in 13″ reel

Temperature Range
- C = Commercial (0°C to +70°C)
- M = Military (−55°C to +125°C)

NOTE:
Not required for MSA package code

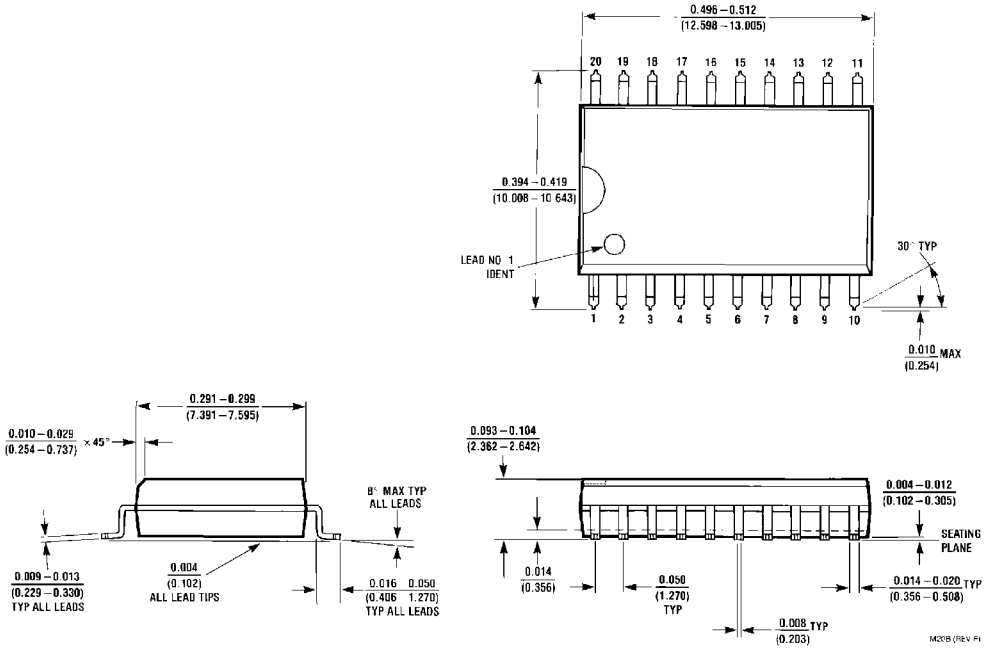
DS009545-8

Physical Dimensions inches (millimeters) unless otherwise noted

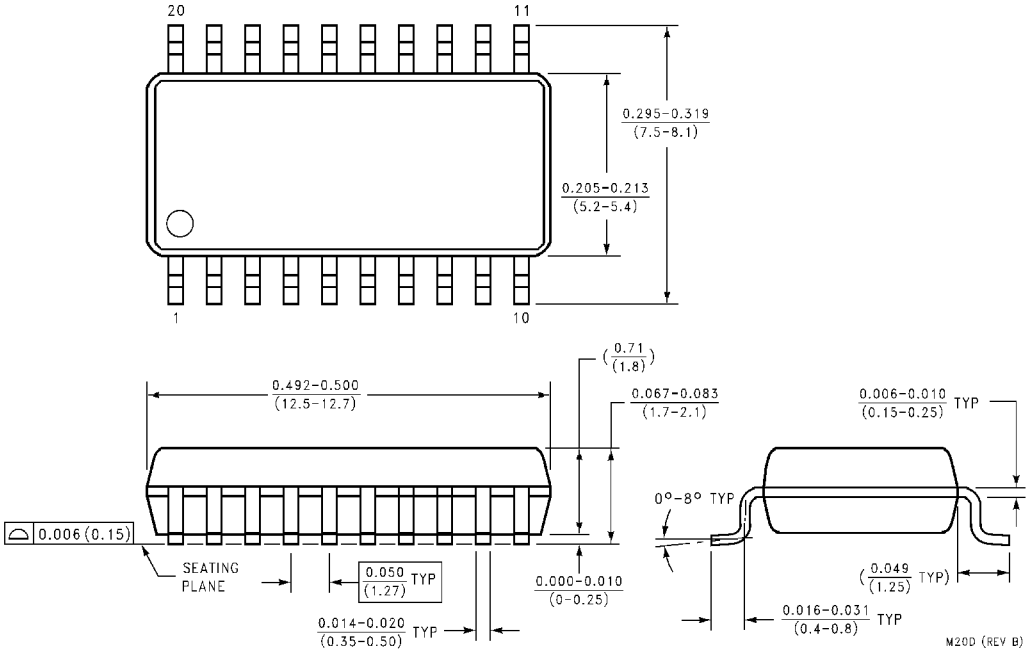


20-Lead Ceramic Dual-In-Line Package (D)
Package Number J20A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

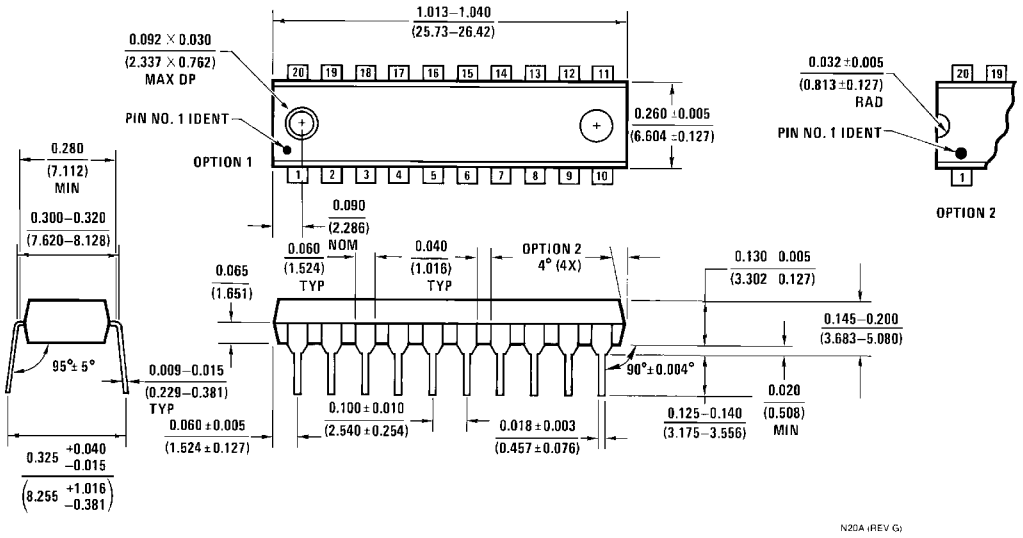


20-Lead (0.300" Wide) Molded Small Outline Package, JEDEC (S)
Package Number M20B

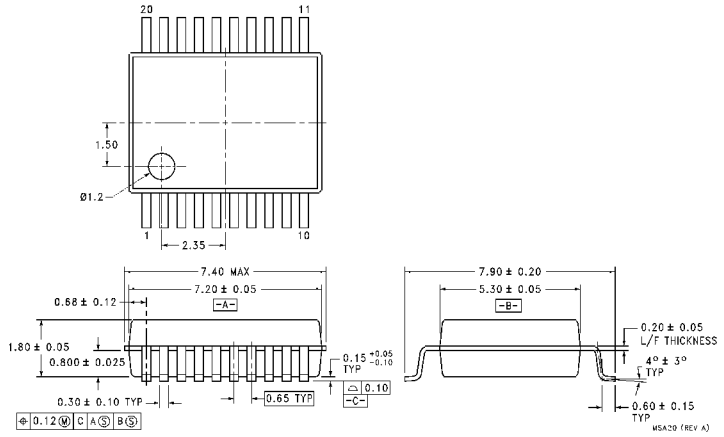


20-Lead (0.300" Wide) Molded Small Outline Package, EIAJ (SJ)
Package Number M20D

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

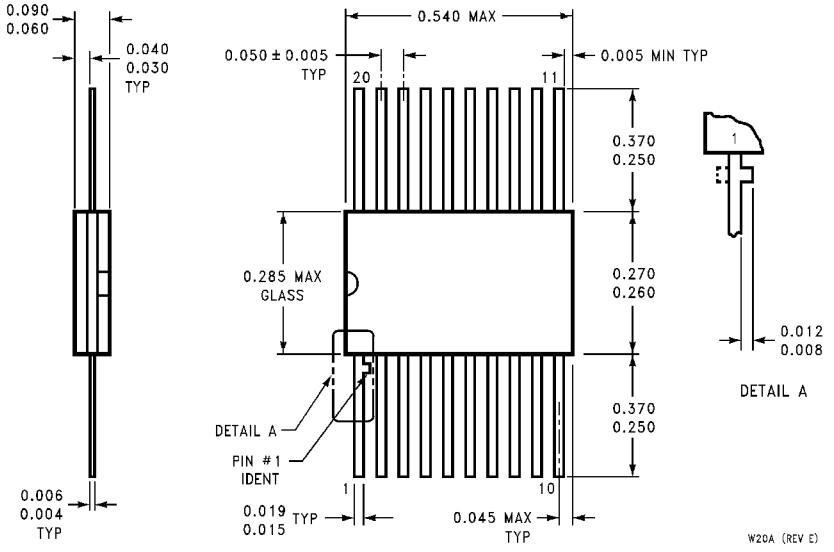


20-Lead (0.300" Wide) Molded Dual-In-Line Package (P)
Package Number N20A



20-Lead (0.300" Wide) Molded Shrink Outline Package, EIAJ, Type II (MSA)
Package Number MSA20

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



20-Lead Ceramic Flatpak (F)
Package Number W20A

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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