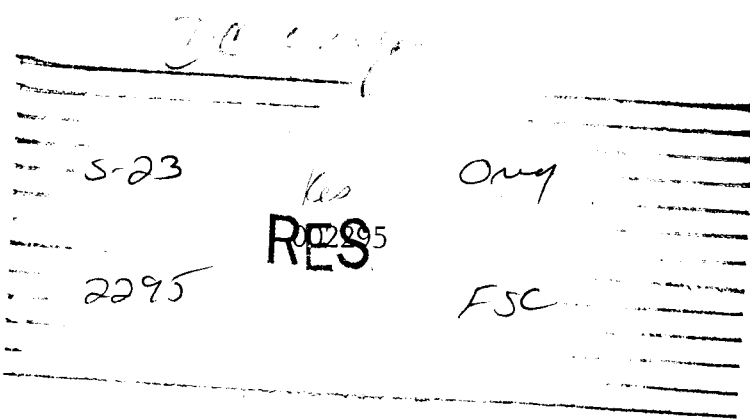


95H02 · 95H03 · 95H04 CLOCK DRIVER/ HIGH SPEED GATES

9505 4 WIDE FSC OR-AND GATE

DC only

"6B"
PRG-



DESCRIPTION The 95H02, H03 and H04 are temperature compensated OR/NOR Gates employing a nonsaturating current switch, emitter follower configuration to achieve a very high speed. The elements are intended for use where higher logic speeds and faster edges than standard 9500 gates are required, as in clock and flip-flop driving.

These gates will improve available speed performance of 9528 flip-flops and 9534 latches in counting and register application.

- HIGH SPEED . . . 1.6 ns PER GATE
- SEPARATE CURRENT SWITCH AND EMITTER FOLLOWER V_{CC} PINS – ELIMINATES NOISE COUPLING
- TEMPERATURE COMPENSATION
- INTERNAL PULLDOWN RESISTORS
- COMMON ENABLE INPUTS
- LOW CROSSTALK AND NOISE GENERATION
- WIRE-OR CAPABILITY
- 50 Ω LINE DRIVING CAPABILITY
- COMPLEMENTARY OR/NOR OUTPUTS (95H02, H03)
- SINGLE -5.2V POWER SUPPLY
- HERMETIC CERAMIC 16 PIN DUAL IN-LINE PACKAGE

DESCRIPTION The 9505 is a temperature compensated OR-AND gate that achieves in slightly over one basic gate delay the AND of four different OR'ed functions.

The NOR outputs of the four OR gates are OR'ed to derive the OR-NOR function.

This element is useful in the design of Arithmetic Logic Units for construction of adders, subtractors, multipliers etc. Just two 9505's will implement a full carry adder function.

- HIGH SPEED . . . 2.7 ns OR-AND, 2.5 ns NOR-OR
- SEPARATE CURRENT SWITCH & EMITTER FOLLOWER V_{CC} PINS – ELIMINATES NOISE COUPLING
- TEMPERATURE COMPENSATED
- INTERNAL 2K PULL DOWN RESISTORS
- COMPLEX LOGIC FUNCTION REDUCES PACKAGE COUNT
- WIRE OR CAPABILITY
- 50 Ω LINE DRIVING CAPABILITY
- SINGLE -5.2V POWER SUPPLY
- HERMETIC CERAMIC 16 PIN DUAL IN-LINE PACKAGE

