

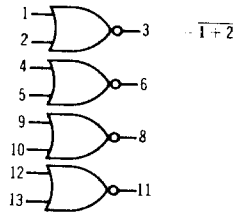
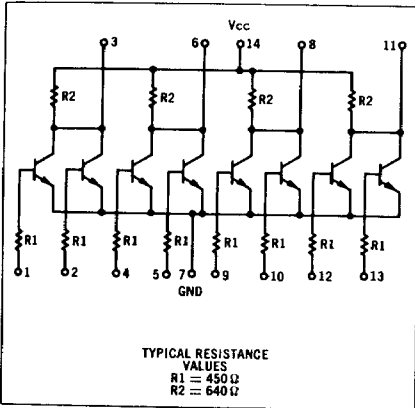
QUAD 2-INPUT GATES

MRTL MC900/800 series

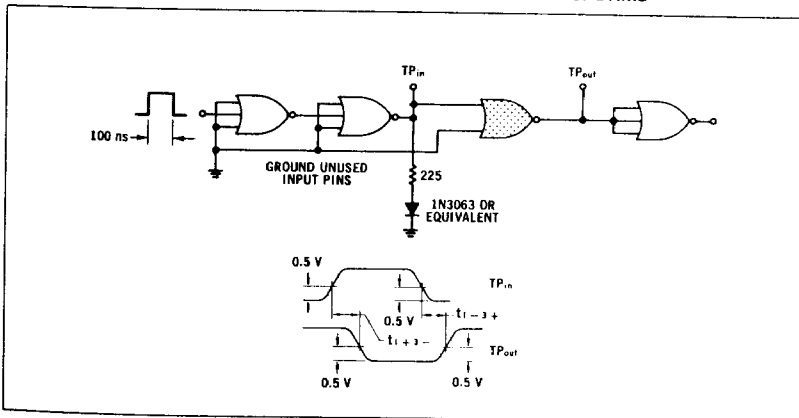
MC924 • MC824 

Available in TO-86 Flat Package, Add "F" Suffix.

This gate element consists of four 2-input positive logic NOR gate circuits in a single package. The gate circuits may be used independently, or connected together to form flip-flops or non-inverting gates.



SWITCHING TIMES TEST CIRCUIT AND WAVEFORMS



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ELECTRICAL CHARACTERISTICS

Test procedures are shown for one gate only. Other gates are tested in the same manner.

Characteristic	Symbol	Pin Under Test	TEST VOLTAGE VALUES														
			@ Test Temperature						TEST VOLTAGE VALUES (Volts)								
			-55°C		+25°C		+125°C		+100°C		V _{in}	V _{on}	V _{off}	V _{AO7}	V _{off}	V _{CC}	
Input Current	I _{in}	1 2	485	435	470	485	435	470	485	435	470	485	435	470	485	435	470
Output Current	I _{A5}	3	2.47	2.54	2.35	2.38	2.52	2.38	2.35	2.38	2.52	2.38	2.35	2.38	2.52	2.38	2.35
Output Leakage Current	I _{CEX}	3	100	218	235	100	225	225	225	225	225	225	225	225	225	225	225
Output Voltage	V _{out}	3	710	300	320	574	400	370	400	370	370	400	370	370	400	370	370
Saturation Voltage	V _{CE(sat)}	3	200	210	280	280	280	280	280	280	280	280	280	280	280	280	280
Switching Time	t	1-3- 1-3-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

Ground inputs of gates not under test. Other pins not listed are left open.

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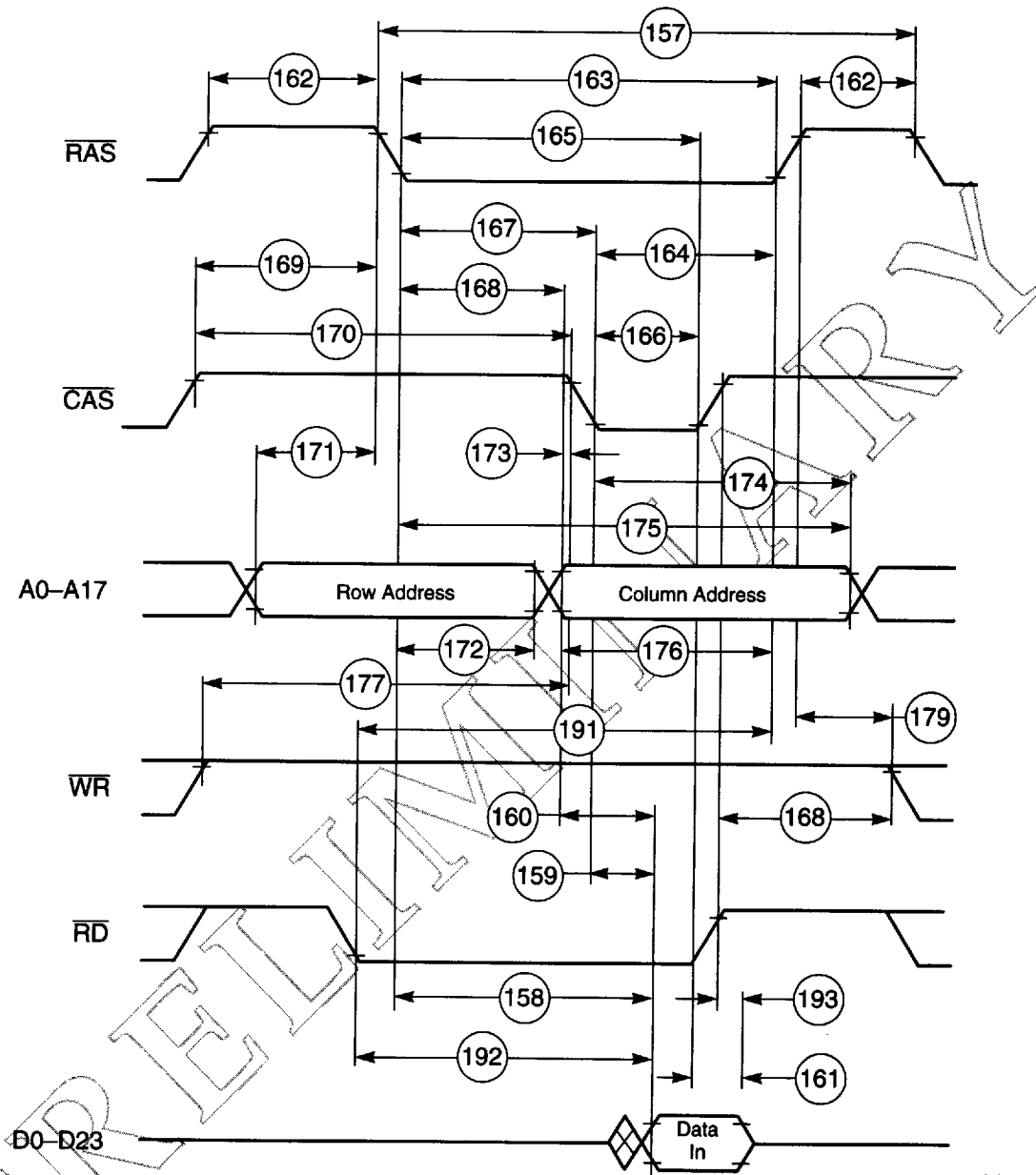


Figure 2-18 DRAM Out-of-Page Read Access

AA0476

Preliminary Data