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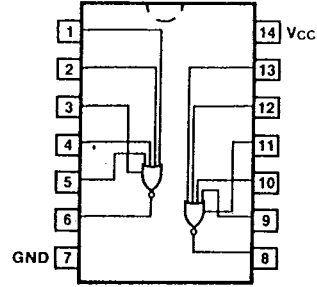
T-43-15

54/7425
DUAL 4-INPUT NOR GATE
 (With Strobe)

CONNECTION DIAGRAM
PINOUT A

ORDERING CODE: See Section 9

| PKGS | PIN OUT | COMMERCIAL GRADE | MILITARY GRADE | PKG OUT |
|-----------------|---------|---|---|---------|
| | | $V_{CC} = +5.0\text{ V} \pm 5\%$, $T_A = 0^\circ\text{C to } +70^\circ\text{C}$ | $V_{CC} = +5.0\text{ V} \pm 10\%$, $T_A = -55^\circ\text{C to } +125^\circ\text{C}$ | |
| Plastic DIP (P) | A | 7425PC | | 9A |
| Ceramic DIP (D) | A | 7425DC | 5425DM | 6A |
| Flatpak (F) | A | 7425FC | 5425FM | 3I |



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

| PINS | 54/74 (U.L.) HIGH/LOW |
|---------------|-----------------------|
| Data Inputs | 1.0/1.0 |
| Strobe Inputs | 4.0/4.0 |
| Outputs | 20/10 |

DC AND AC CHARACTERISTICS: See Section 3*

| SYMBOL | PARAMETER | 54/74 | | UNITS | CONDITIONS | |
|------------------------|----------------------|-------|----------|-------|------------------------|-----------------------|
| | | Min | Max | | | |
| I_{CCH} | Power Supply Current | | 16 | mA | $V_{IN} = \text{Gnd}$ | $V_{CC} = \text{Max}$ |
| I_{CCL} | | | 19 | | $V_{IN} = \text{Open}$ | |
| t_{PLH} t_{PHL} | Propagation Delay | | 22 15 | ns | Figs. 3-1, 3-4 | |

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ\text{C}$ and $V_{CC} = +5.0\text{ V}$.