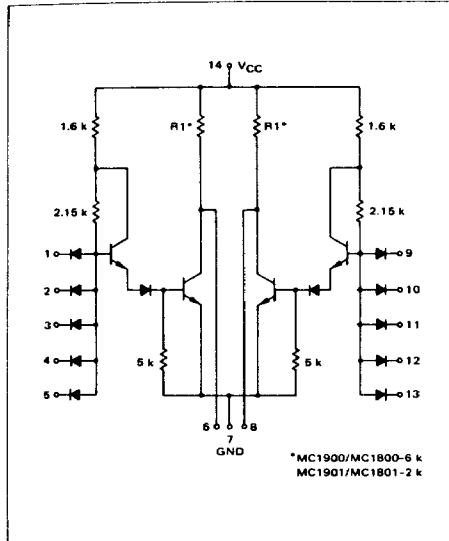


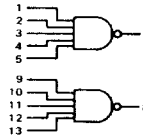
DUAL 5-INPUT "NAND" GATE

MDTL MC930/830 series

MC1900F · MC1800F, P
MC1901F · MC1801F, P



This gate element, in the 14-pin flat and dual in-line packages, consists of two 5-input NAND gate circuits. The elements may be cross-coupled to form a bistable multivibrator, or the outputs may be connected in parallel to perform the logic "OR" function.



Positive Logic: 6 = 1, 2, 3, 4, 5

Negative Logic: 6 = 1, 2, 3, 4, 5

Input Loading Factor = 1

Output Loading Factor:

MC1900/MC1800 = 8

MC1901/MC1801 = 7

Total Power Dissipation:

MC1900/MC1800 = 22 mW typ/pkg

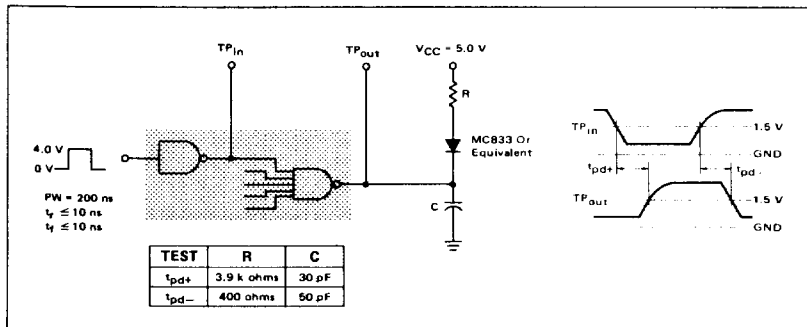
MC1901/MC1801 = 33 mW typ/pkg

Propagation Delay Time:

MC1900/MC1800 = 30 ns typ

MC1901/MC1801 = 25 ns typ

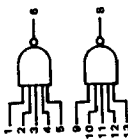
SWITCHING TIME TEST CIRCUIT AND WAVEFORMS



MC1900F/MC1800F, P, MC1901F/MC1801F, P (continued)

ELECTRICAL CHARACTERISTICS

Test procedures are shown for only one gate. The other gates in tested in the same manner.



Characteristic	Symbol	MC1900, MC1800 Test Limits										MC1901, MC1801 Test Limits										MC1900, MC1800 Test Limits										MC1901, MC1801 Test Limits									
		-55°C		+25°C		+125°C		0°C		+25°C		+75°C		-55°C		+25°C		+125°C		0°C		+25°C		+75°C		-55°C		+25°C		+125°C		0°C		+25°C		+75°C					
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max								
Output Voltage	V _{OL}	0	0.40	0.40	0.40	0.45	V _{CC}	0.45	0.45	0.45	0.50	V _{CC}	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45	0.45								
	V _{OH}	2.50	2.60	2.60	3.50	V _{CC}	2.60	2.60	2.60	2.60	2.50	V _{CC}	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60	2.60								
Short-Circuit Current	I _{SC}	-1.34	-1.34	-1.34	-1.30	mA	-1.30	-1.30	-1.30	-1.30	-1.25	mA	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30	-1.30							
Reverse Current	I _R	2.0	2.0	2.0	5.0	μA	5.0	5.0	5.0	5.0	10	μA	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0							
Output Leakage Current	I _{CEX}	0	0	0	50	μA	50	50	50	100	μA	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50						
Forward Current	I _F	-1.60	-1.60	-1.60	-1.50	mA	-1.60	-1.60	-1.60	-1.60	-1.30	mA	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60	-1.60						
Power Drain Current	I _{PDH}	0	0	0	6.5	mA	6.5	6.5	6.5	6.0	mA	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5						
	I _{PDH}	0	0	0	10.7	mA	10.7	10.7	10.7	10.1	mA	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7	10.7						
	I _{PDH}	0	0	0	5.5	mA	5.5	5.5	5.5	5.0	mA	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5	5.5							
Switching Times	t _{pd+}	1.6	1.6	1.6	80	ns	80	80	80	25	ns	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80	80						
	t _{pd-}	1.6	1.6	1.6	10.35	ns	10.35	10.35	10.35	10	ns	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35						
	t _{pd+}	1.6	1.6	1.6	15.60	ns	15.60	15.60	15.60	15	ns	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60	15.60						
	t _{pd-}	1.6	1.6	1.6	10.35	ns	10.35	10.35	10.35	10	ns	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35	10.35					

Pin not listed are left open.

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PRODUCT DOCUMENTATION

The three documents listed in the following table are required for a complete description of the DSP56301 and are necessary to design properly with the part. Documentation is available from one of the following locations (see back cover for detailed information):

- A local Motorola distributor
- A Motorola semiconductor sales office
- A Motorola Literature Distribution Center
- The World Wide Web (WWW)

See the **Additional Support** section of the *DSP56300 Family Manual* for detailed information on the multiple support options available to you.

Table 1 DSP56301 Documentation

Name	Description	Order Number
DSP56300 Family Manual	Detailed description of the DSP56300 family processor core and instruction set	DSP56300FM/AD
DSP56301 User's Manual	Detailed functional description of the DSP56301 memory configuration, operation, and register programming	DSP56301UM/AD
DSP56301 Technical Data	DSP56301 features list and physical, electrical, timing, and package specifications	DSP56301/D

