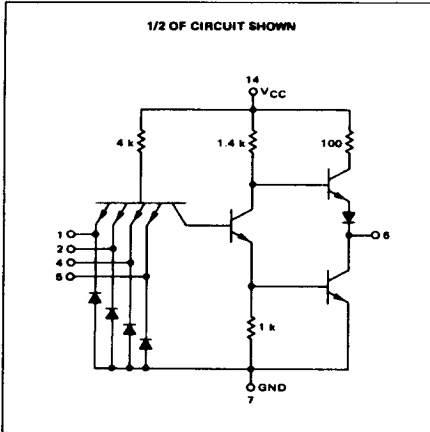


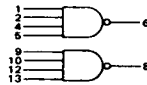
DUAL 4-INPUT "NAND" GATE

MTTL MC7400P series
MTTL MC5400L/7400L series

MC5420L*
MC7420P,L*



This device consists of two 4-input NAND gates. These gates may be cross-coupled to form a set-reset flip-flop.



Positive Logic: $6 = 1 + 2 + 4 + 5$
Negative Logic: $6 = 1 + 2 + 4 + 5$

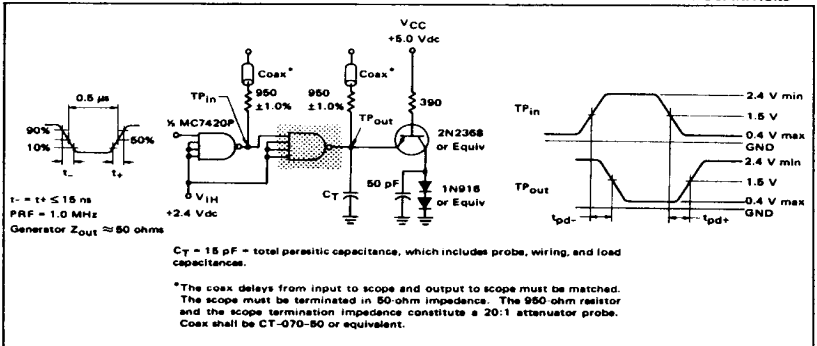
Input Loading Factor = 1
Output Loading Factor = 10

Total Power Dissipation = 20 mW typ/pkg
Propagation Delay Time = 13 ns typ

*L suffix = TO-118 ceramic package (Case 632)
P suffix = TO-118 plastic package (Case 606)
See General Information section for package outline dimensions.

SWITCHING TIME TEST CIRCUIT

VOLTAGE WAVEFORMS AND DEFINITIONS



377

377

