

Power line chokes

I core chokes

500 V AC, 1 ... 25 A, 0.065 ... 27 mH

Series/Type: **B82504W**

Date: July 2012

© EPCOS AG 2015. Reproduction, publication and dissemination of this publication, enclosures hereto and the information contained therein without EPCOS' prior express consent is prohibited.

EPCOS AG is a TDK Group Company.

Power line chokes

B82504W

I core chokes

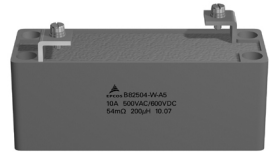
Rated voltage 500 V AC/600 V DC

Rated current 1 ... 25 A

Rated inductance 0.065 ... 27 mH

Construction

- I core choke
- Rectangular plastic case
- Resin potting



Features

- Low power dissipation
- Suppression of broadband interference
- Compact design
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2)
- RoHS-compatible

Applications

- Suppression of symmetrical and asymmetrical interference
- High-performance power supplies
- Industrial applications

Terminals

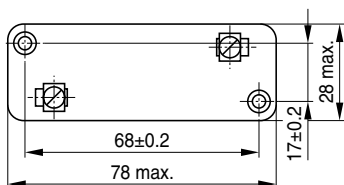
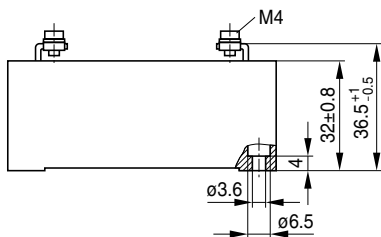
Screw terminals M4

Marking

Manufacturer, ordering code, rated inductance, rated voltage, rated current, DC resistance, date of manufacture (MM.YY)

Delivery mode

Cardboard box

Dimensional drawing (dimensions in mm)


IND0461-C

 Tolerances to ISO 2768-C
 unless otherwise noted.

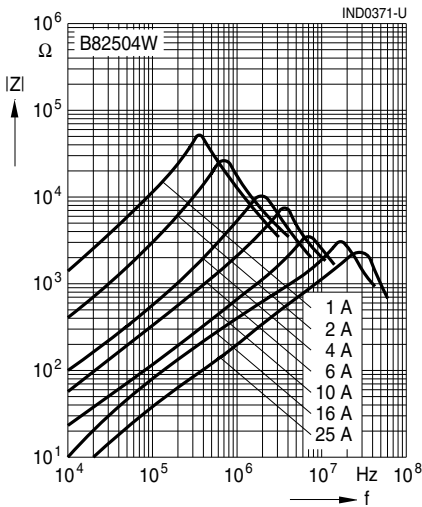
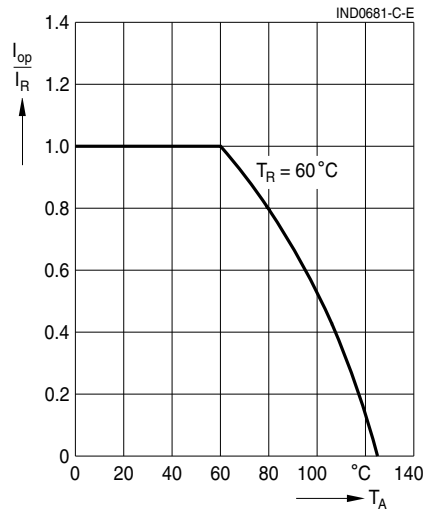
Dimensions in mm

Technical data and measuring conditions

Rated voltage V_R	500 V AC (50/60 Hz) / 600 V DC During operation between winding and metal parts (VDE 0565-2).
Test voltage V_{test}	2800 V AC, 2 s (winding/core) 2800 V AC, 2 s (winding/case)
Rated temperature T_R	+60 °C
Rated current I_R	Referred to 50 Hz and rated temperature
Permissible operating current at 400 Hz	$0.6 \cdot I_R$
Rated inductance L_R	Measured with Agilent 4284A at 0.1 mA, +20 °C Measuring frequency: $L_R \leq 1$ mH = 100 kHz $L_R > 1$ mH = 10 kHz
Inductance tolerance	±20% at +20 °C
DC resistance R_{typ}	Measured at +20 °C, typical values
Storage conditions (packaged)	-25 °C ... +40 °C, ≤ 75% RH
Climatic category	40/125/56 (to IEC 60068-1)
Weight	Approx. 170 ... 230 g

Characteristics and ordering codes

I_R A	L_R mH	R_{typ} Ω	Ordering code
1	27	5.25	B82504W0000A001
2	7.5	1.3	B82504W0000A002
4	2.0	0.33	B82504W0000A003
5	1.2	0.20	B82504W0000A009
6	0.6	0.15	B82504W0000A004
10	0.2	0.054	B82504W0000A005
16	0.12	0.019	B82504W0000A016
25	0.065	0.009	B82504W0000A007

Impedance $|Z|$ versus frequency f
 measured at +20 °C, typical values

Current derating I_{op}/I_R
 versus ambient temperature T_A


Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
 - Particular attention should be paid to the derating curves given there. Derating must be applied in case the ambient temperature in the application exceeds the rated temperature of the component.
 - Ensure the operation temperature (which is the sum of the ambient temperature and the temperature rise caused by losses / self-heating) of the component in the application does not exceed the maximum value specified in the climatic category.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
 - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
 - The effect of the potting material can change the high-frequency behaviour of the components.
- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous)**. Useful information on this will be found in our Material Data Sheets on the Internet (www.epcos.com/material). Should you have any more detailed questions, please contact our sales offices.
5. We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.

We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available. The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.
6. Unless otherwise agreed in individual contracts, **all orders are subject to the current version of the "General Terms of Delivery for Products and Services in the Electrical Industry" published by the German Electrical and Electronics Industry Association (ZVEI)**.
7. The trade names EPCOS, BAOKE, Alu-X, CeraDiode, CeraLink, CSMP, CSSP, CTVS, DeltaCap, DigiSiMic, DSSP, FilterCap, FormFit, MiniBlue, MiniCell, MKD, MKK, MLSC, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SIP5D, SIP5K, ThermoFuse, WindCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at www.epcos.com/trademarks.