



# TEA1733AT

## GreenChip SMPS control IC

Rev. 3 — 24 May 2013

Product data sheet

## 1. General description

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The TEA1733AT is a low cost Switched Mode Power Supply (SMPS) controller IC intended for flyback topologies. It operates in fixed frequency mode. Frequency jitter has been implemented to reduce ElectroMagnetic Interference (EMI). Slope compensation is integrated for Continuous Conduction Mode (CCM) operation.

The TEA1733AT IC includes OverPower Protection (OPP). This enables the controller to operate under overpower situations for a limited amount of time.

Two pins, VINSENSE and PROTECT, are reserved for protection purposes. Input UnderVoltage Protection (UVP) and OverVoltage Protection (OVP), output OVP and OverTemperature Protection (OTP) can be implemented using a minimal number of external components.

At low power levels the primary peak current is set to 25 % of the maximum peak current and the switching frequency is reduced to limit switching losses. The combination of fixed frequency operation at high output power and frequency reduction at low output power provides high-efficiency over the total load range.

The TEA1733AT enables low cost, highly efficient and reliable supplies for power requirements up to 75 W to be designed easily and with a minimum number of external components.

## 2. Features and benefits

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### 2.1 Features

- SMPS controller IC enabling low-cost applications
- Large input voltage range (12 V to 30 V)
- Very low supply current during start-up and restart (typically 10  $\mu$ A)
- Low supply current during normal operation (typically 0.55 mA without load)
- Overpower or high/low line compensation
- Adjustable overpower time-out
- Adjustable overpower restart timer
- Fixed switching frequency with frequency jitter to reduce EMI
- Frequency reduction with fixed minimum peak current to maintain high-efficiency at low output power levels
- Slope compensation for CCM operation
- Low and adjustable OverCurrent Protection (OCP) trip level



- Adjustable soft start operation
- Two protection inputs (e.g. for input UVP and OVP, OTP and output OVP)
- IC overtemperature protection

### 3. Applications

- All applications requiring efficient and cost-effective power supply solutions up to 75 W.

### 4. Ordering information

Table 1. Ordering information

| Type number | Package |   | Version |
|-------------|---------|---|---------|
|             | Name    | Description   |         |
| TEA1733AT   | SO8     | plastic small outline package; 8 leads; body width 3.9 mm | SOT96-1 |

### 5. Block diagram

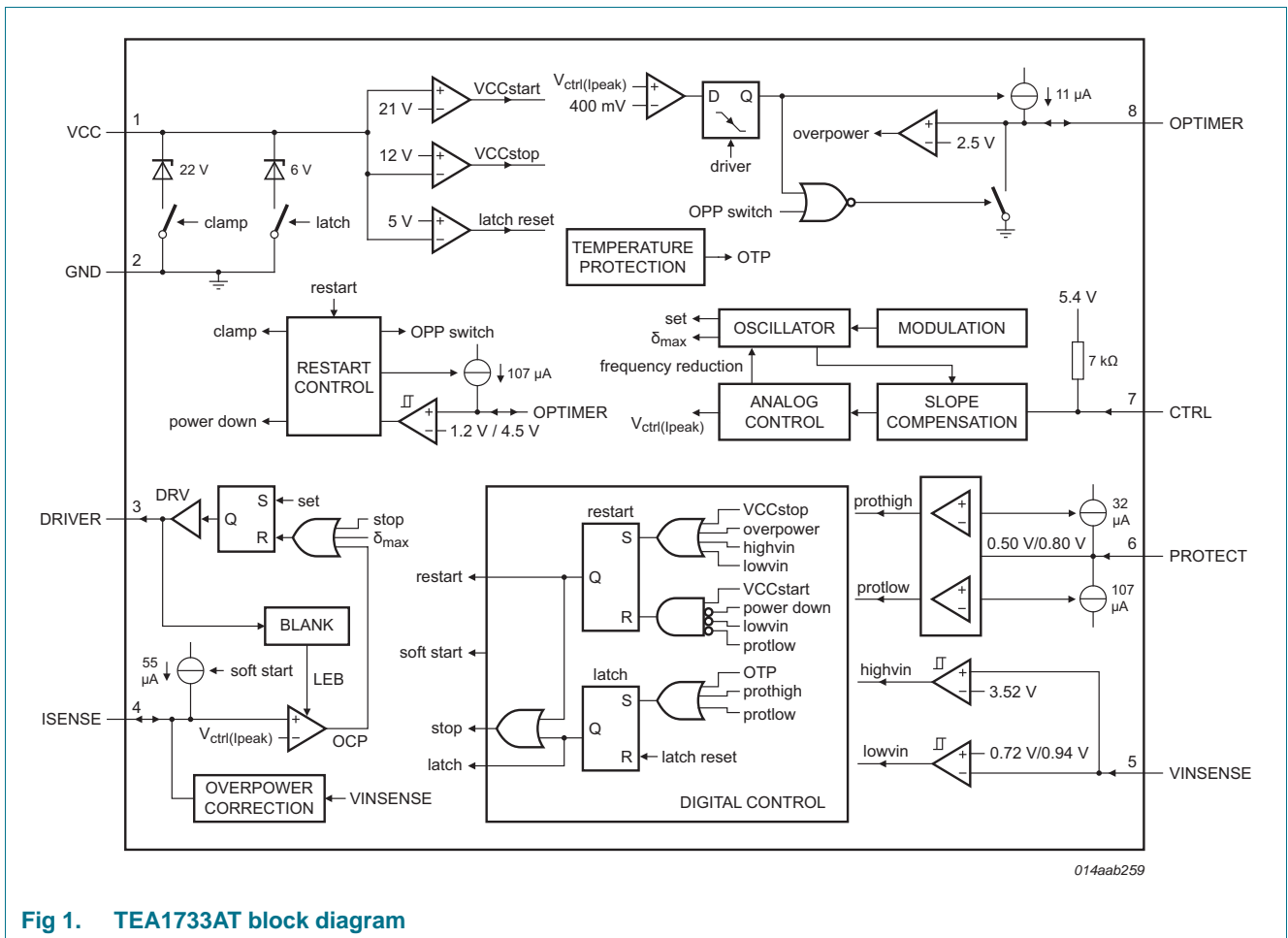
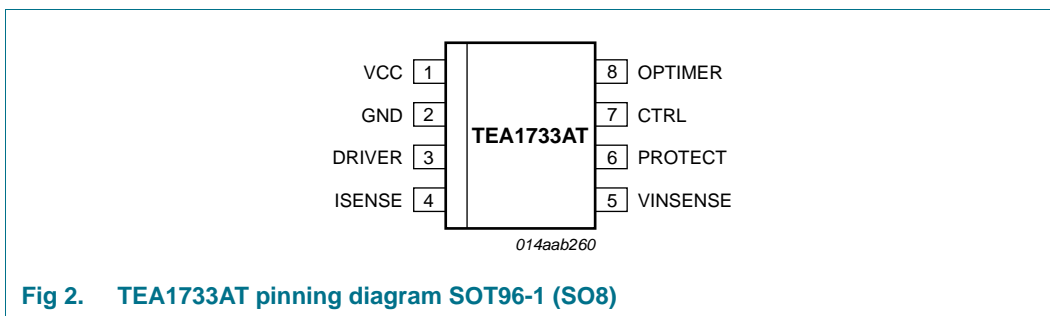


Fig 1. TEA1733AT block diagram

## 6. Pinning information

### 6.1 Pinning



### 6.2 Pin description

Table 2. Pin description

| Symbol   | Pin | Description                      |
|----------|-----|----------------------------------|
| VCC      | 1   | supply voltage                   |
| GND      | 2   | ground                           |
| DRIVER   | 3   | gate driver output               |
| ISENSE   | 4   | current sense input              |
| VINSENSE | 5   | input voltage protection input   |
| PROTECT  | 6   | general purpose protection input |
| CTRL     | 7   | control input                    |
| OPTIMER  | 8   | overpower and restart timer      |

## 7. Functional description

### 7.1 General control

The TEA1733AT contains a flyback circuit controller, a typical configuration of which is shown in [Figure 3](#).

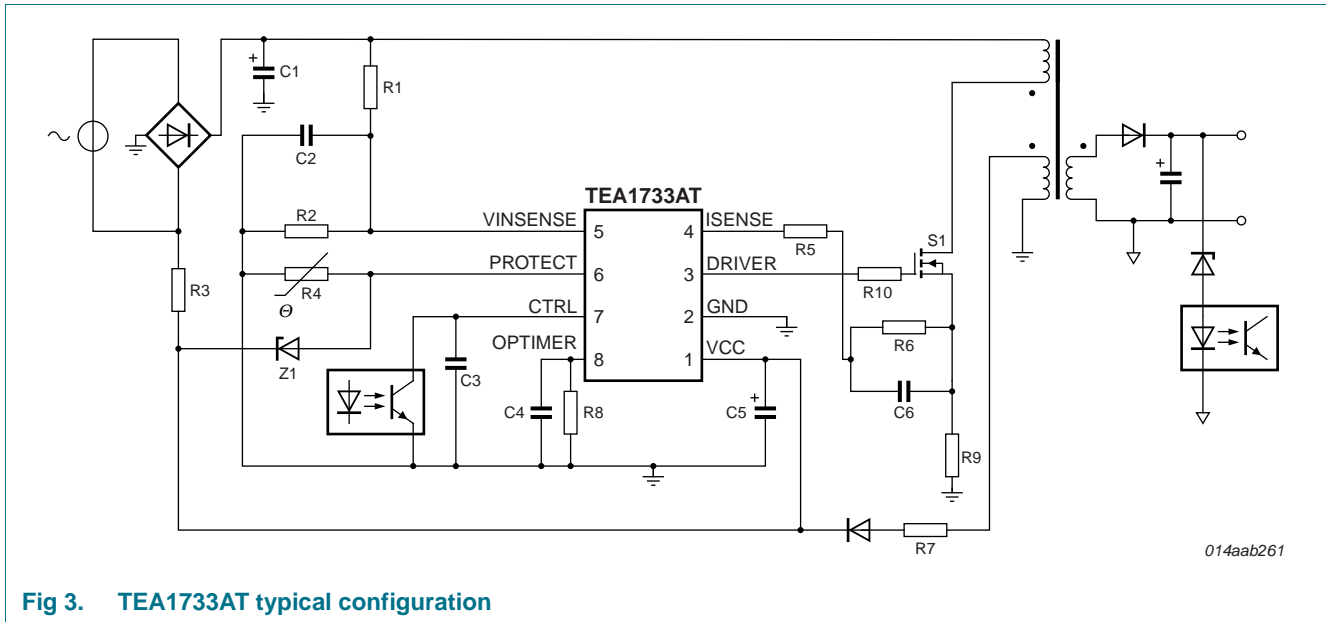


Fig 3. TEA1733AT typical configuration

### 7.2 Start-up and UnderVoltage LockOut (UVLO)

Initially, the capacitor on the VCC pin is charged from the high voltage mains via resistor R3.

If  $V_{CC}$  is lower than  $V_{start-up}$ , the IC current consumption is low (typically 10  $\mu$ A). When  $V_{CC}$  reaches  $V_{start-up}$  the IC first waits for pin VINSENSE to reach the  $V_{start(VINSENSE)}$  voltage and for pin PROTECT to reach the  $V_{det(L)(PROTECT)}$  voltage. When both levels are reached, the IC charges the ISENSE pin to the  $V_{start(soft)}$  level and starts switching. In a typical application the supply voltage is taken over by the auxiliary winding of the transformer.

If a protection is triggered the controller stops switching. Depending on the protection triggered the protection either causes a restart or latches the converter to an off-state.

A restart caused by a protection rapidly charges the OPTIMER pin to 4.5 V (typical). The TEA1733AT enters the Power-down mode until the OPTIMER pin discharges down to 1.2 V (typical). In Power-down mode, the IC consumes a very low supply current (10  $\mu$ A typical) and the VCC pin is clamped at 22 V (typical) by an internal clamp circuit. When the voltage on pin OPTIMER drops below 1.2 V (typical) and the VCC pin voltage is above the VCC start-up voltage (See [Figure 4](#)), the IC restarts.

When a latched protection is triggered, the TEA1733AT immediately enters Power-down mode. The VCC pin is clamped to a voltage just above the latch protection reset voltage ( $V_{rst(latch)} + 1$  V).

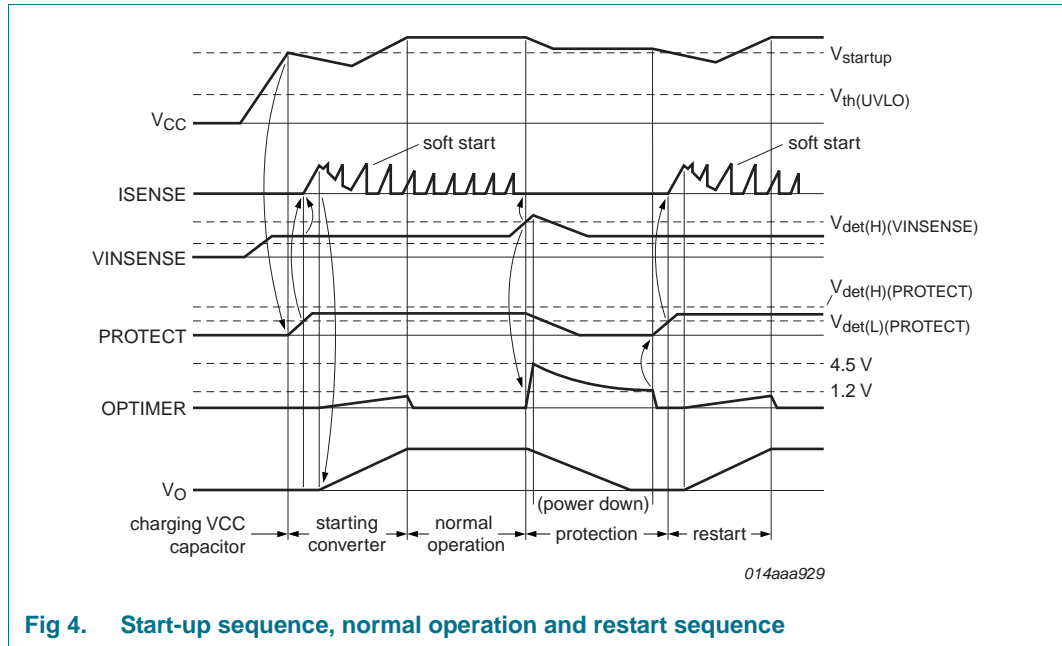


Fig 4. Start-up sequence, normal operation and restart sequence

When the voltage on pin VCC drops below the UVLO level during normal operation, the controller stops switching and enters Restart mode. In Restart mode, the driver output is disabled and the VCC pin voltage is recharged via resistor R3 to the rectified mains voltage.

### 7.3 Supply management

All internal reference voltages are derived from a temperature compensated on-chip band gap circuit. Internal reference currents are derived from a trimmed and temperature compensated current reference circuit.

### 7.4 Input voltage detection (VINSENSE pin)

In a typical application the mains input voltage can be detected by the VINSENSE pin. Switching will not take place until the voltage on VINSENSE has reached the  $V_{start(VINSENSE)}$  voltage (typically 0.94 V).

When the VINSENSE voltage drops below  $V_{det(L)(VINSENSE)}$  (typically 0.72 V) or exceeds  $V_{det(H)(VINSENSE)}$  (typically 3.52 V), the converter stops switching and performs a restart.

If pin VINSENSE is left open or disconnected, the pin is pulled up by the internal 20 nA (typical) current source to reach the  $V_{det(H)(VINSENSE)}$  level. This triggers a restart protection.

An internal clamp of 5.2 V (typical) protects this pin from excessive voltages.

### 7.5 Protection input (PROTECT pin)

Pin PROTECT is a general purpose input pin, which can be used to switch off the converter (latched protection). The converter is stopped when the voltage on this pin is pulled above  $V_{det(H)(PROTECT)}$  (typically 0.8 V) or below  $V_{det(L)(PROTECT)}$  (typically 0.5 V). A

current of 32  $\mu\text{A}$  (typical) flows out of the chip when the pin voltage is at the  $V_{\text{det(L)}(\text{PROTECT})}$  level. A current of 107  $\mu\text{A}$  (typical) flows into the chip when the pin voltage is at the  $V_{\text{det(H)}(\text{PROTECT})}$  level.

The PROTECT input can be used to create an overvoltage detection and OTP functions.

A small capacitor can be connected to the pin if the protections on this pin are not used.

An internal clamp of 4.1 V (typical) protects this pin from excessive voltages.

### 7.6 Duty cycle control (CTRL pin)

The output power of the converter is regulated by the CTRL pin. This pin is connected to an internal 5.4 V supply using an internal 7 k $\Omega$  resistor.

The CTRL pin voltage sets the peak current which is measured using the ISENSE pin (see Section 7.9). At low output power, the switching frequency is also reduced (see Section 7.12). The maximum duty cycle is limited to 72 % (typical).

### 7.7 Slope compensation (CTRL pin)

A slope compensation circuit is integrated in the IC for CCM. Slope compensation guarantees stable operation for duty cycles greater than 50 %.

### 7.8 Overpower timer (OPTIMER pin)

If the OPTIMER pin is connected to capacitor C4 (see Figure 3), a temporary overload situation is allowed.  $V_{\text{ctrl}(I_{\text{peak}})}$  (see Figure 1) is set by pin CTRL. When  $V_{\text{ctrl}(I_{\text{peak}})}$  is above 400 mV, the  $I_{\text{O}(\text{OPTIMER})}$  current (11  $\mu\text{A}$  typical) is sourced from the OPTIMER pin. If the voltage on the OPTIMER pin reaches the  $V_{\text{prot}(\text{OPTIMER})}$  voltage (2.5 V typical) the OverPower Protection (OPP) is triggered (see Figure 5).

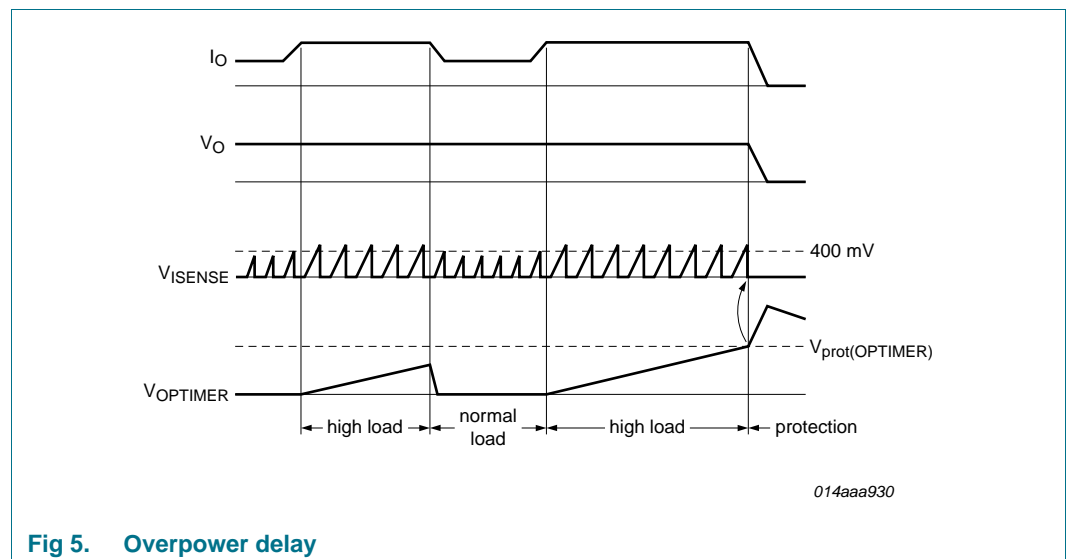


Fig 5. Overpower delay

When the  $V_{\text{prot}(\text{OPTIMER})}$  voltage is reached the device restarts.

If the overload is removed before the  $V_{\text{prot}(\text{OPTIMER})}$  voltage is reached, the converter will continue switching.

### 7.9 Current mode control (ISENSE pin)

Current mode control is used for its good line regulation.

The primary current is sensed by the ISENSE pin across an external resistor R9 (see [Figure 3](#)) and compared with an internal control voltage. The internal control voltage is proportional to the CTRL pin voltage (see [Figure 6](#)).

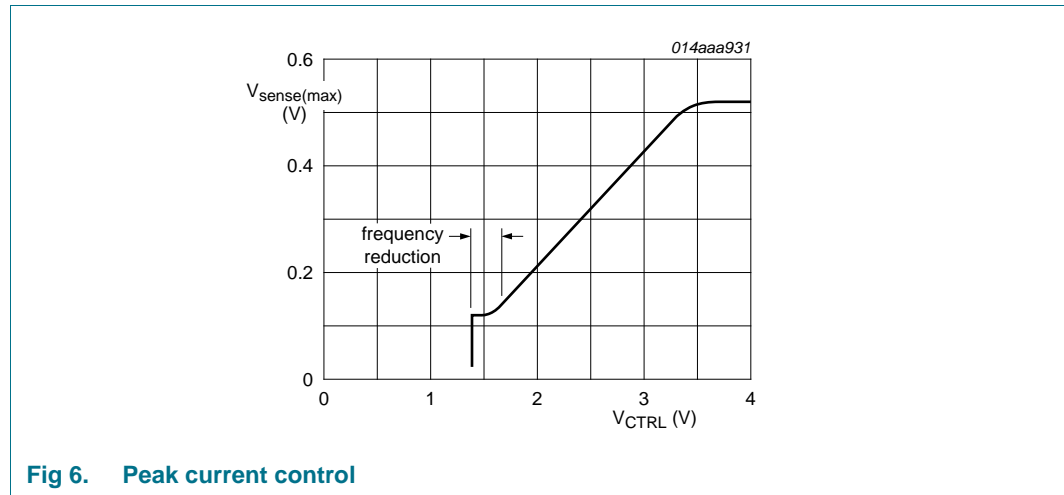


Fig 6. Peak current control

Leading edge blanking prevents false triggering due to capacitive discharge when switching on the external power switch (see [Figure 7](#)).

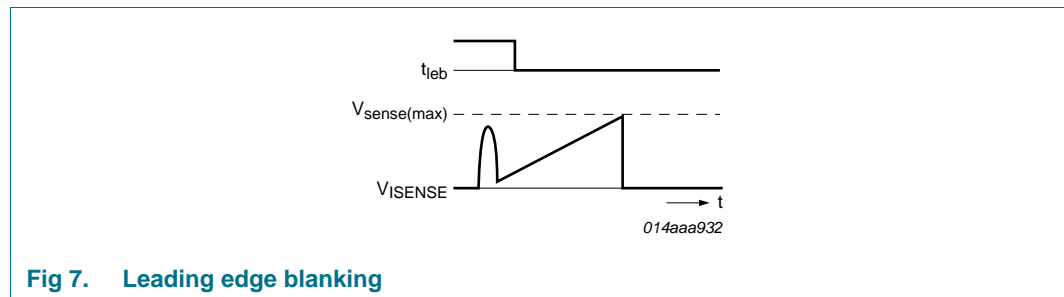


Fig 7. Leading edge blanking

### 7.10 Overpower or high/low line compensation (VINSENSE and ISENSE pins)

The overpower compensation function can be used to realize a maximum output power which is nearly constant over the full input mains.

The overpower compensation circuit measures the input voltage on the VINSENSE pin and outputs a proportionally dependent current on the ISENSE pin. The DC voltage across the soft start resistor limits the maximum peak current on the current sense resistor (see [Figure 8](#)).

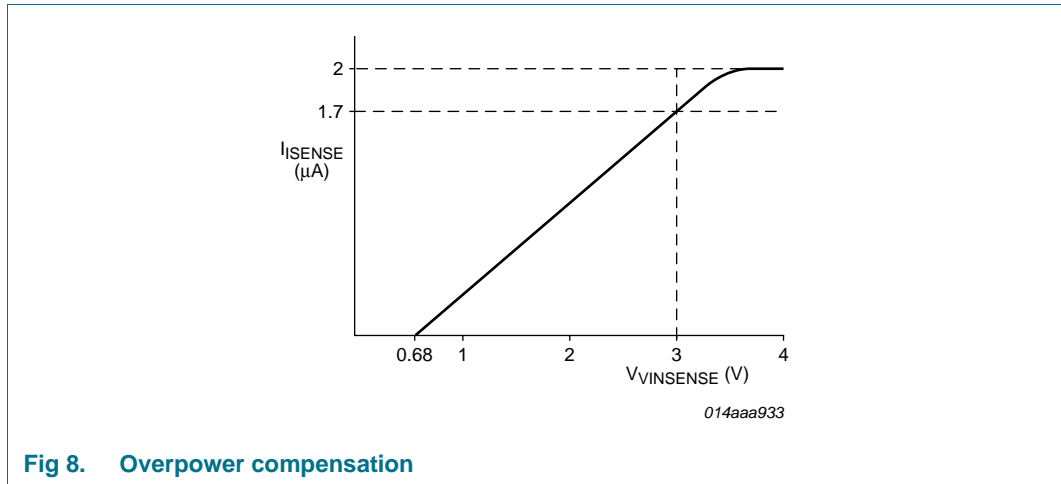


Fig 8. Overpower compensation

At low output power levels the overpower compensation circuit is switched off.

### 7.11 Soft start-up (ISENSE pin)

A soft start is made to prevent audible noise during start-up or a restart condition. Before the converter starts, the soft start capacitor C6 (see [Figure 3](#)) on the ISENSE pin is charged. When the converter starts switching, the primary peak current slowly increases as the soft start capacitor discharges through the soft start resistor (R6, see [Figure 3](#)).

The soft start time constant is set by the soft start capacitor value chosen. The soft start resistor value must also be taken into account, but this value is typically defined by the overpower compensation (see [Section 7.10](#)).

### 7.12 Low power operation

In low power operation switching losses are reduced by lowering the switching frequency. The converter switching frequency is reduced and the peak current is set to 25 % of the maximum peak current (see [Figure 6](#) and [Figure 9](#)).

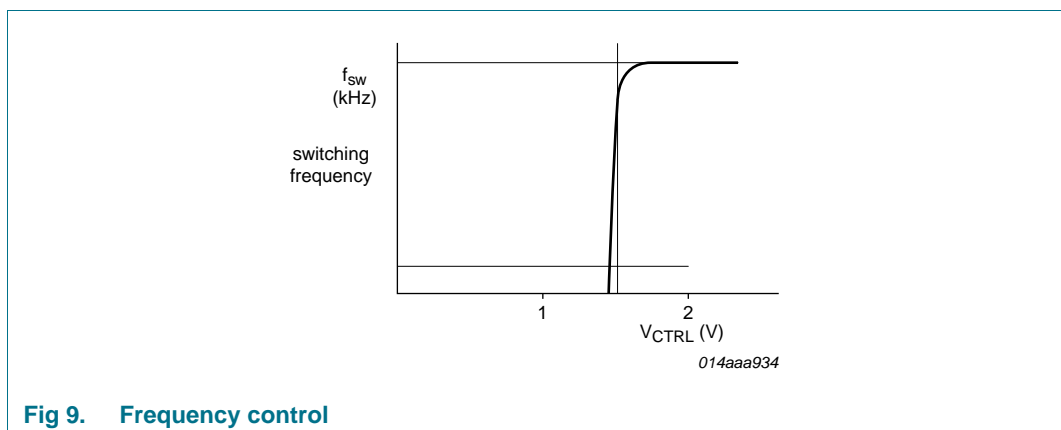


Fig 9. Frequency control



### 7.13 Driver (pin DRIVER)

The driver circuit to the gate of the power MOSFET has a current sourcing capability of typically  $-0.3$  A and a current sink capability of typically  $0.75$  A. This allows for a fast turn-on and turn-off of the power MOSFET for efficient operation.

### 7.14 OverTemperature Protection (OTP)

Integrated temperature protection ensures the IC stops switching if the junction temperature exceeds the thermal shutdown temperature limit.

OTP is a latched protection and it can be reset by removing the voltage on pin VCC.

## 8. Limiting values

**Table 3. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol            | Parameter                           | Conditions           | Min  | Max  | Unit |   |
|-------------------|-------------------------------------|----------------------|------|------|------|---|
| <b>Voltages</b>   |                                     |                      |      |      |      |   |
| $V_{CC}$          | supply voltage                      | continuous           | -0.4 | +30  | V    |   |
|                   |                                     | $t < 100$ ms         | -    | 35   | V    |   |
| $V_{VINSENSE}$    | voltage on pin VINSENSE             | current limited      | -0.4 | +5.5 | V    |   |
| $V_{PROTECT}$     | voltage on pin PROTECT              | current limited      | -0.4 | +5   | V    |   |
| $V_{CTRL}$        | voltage on pin CTRL                 |                      | -0.4 | +5.5 | V    |   |
| $V_{IO(OPTIMER)}$ | input/output voltage on pin OPTIMER |                      | -0.4 | +5   | V    |   |
| $V_{ISENSE}$      | voltage on pin ISENSE               | current limited      | -0.4 | +5   | V    |   |
| <b>Currents</b>   |                                     |                      |      |      |      |   |
| $I_{I(VINSENSE)}$ | input current on pin VINSENSE       |                      | -1   | +1   | mA   |   |
| $I_{I(PROTECT)}$  | input current on pin PROTECT        |                      | -1   | +1   | mA   |   |
| $I_{CTRL}$        | current on pin CTRL                 |                      | -3   | 0    | mA   |   |
| $I_{ISENSE}$      | current on pin ISENSE               |                      | -10  | +1   | mA   |   |
| $I_{DRIVER}$      | current on pin DRIVER               | $\delta < 10\%$      | -0.4 | +1   | A    |   |
| <b>General</b>    |                                     |                      |      |      |      |   |
| $P_{tot}$         | total power dissipation             | $T_{amb} < 75$ °C    | -    | 0.5  | W    |   |
| $T_{stg}$         | storage temperature                 |                      | -55  | +150 | °C   |   |
| $T_j$             | junction temperature                |                      | -40  | +150 | °C   |   |
| <b>ESD</b>        |                                     |                      |      |      |      |   |
| $V_{ESD}$         | electrostatic discharge voltage     | class 1              |      |      |      |   |
|                   |                                     | human body model     | [1]  | -    | 4000 | V |
|                   |                                     | machine model        | [2]  | -    | 300  | V |
|                   |                                     | charged device model |      | -    | 750  | V |

[1] Equivalent to discharging a 100 pF capacitor through a 1.5 k $\Omega$  series resistor.

[2] Equivalent to discharging a 200 pF capacitor through a 0.75  $\mu$ H coil and a 10  $\Omega$  resistor.

## 9. Thermal characteristics

**Table 4. Thermal characteristics**

| Symbol        | Parameter                                   | Conditions                    | Typ | Unit |
|---------------|---|-------------------------------|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air; JEDEC test board | 150 | K/W  |
| $R_{th(j-c)}$ | thermal resistance from junction to case    | in free air; JEDEC test board | 79  | K/W  |

## 10. Characteristics

**Table 5. Characteristics**

$T_{amb} = 25\text{ °C}$ ;  $V_{CC} = 20\text{ V}$ ; all voltages are measured with respect to ground (pin 2); currents are positive when flowing into the IC; unless otherwise specified.

| Symbol                                      | Parameter                                    | Conditions                          | Min  | Typ                  | Max  | Unit          |
|---|--|-------------------------------------|------|----------------------|------|---------------|
| <b>Supply voltage management (pin VCC)</b>  |  |                                     |      |                      |      |               |
| $V_{startup}$                               | start-up voltage                             |                                     | 18.6 | 20.6                 | 22.6 | V             |
| $V_{th(UVLO)}$                              | undervoltage lockout threshold voltage       |                                     | 11.2 | 12.2                 | 13.2 | V             |
| $V_{clamp(VCC)}$                            | clamp voltage on pin VCC                     | activated during restart            | -    | $V_{startup} + 1$    | -    | V             |
|   |  | activated during latched protection | -    | $V_{rst(latch)} + 1$ | -    | V             |
| $V_{hys}$                                   | hysteresis voltage                           | $V_{startup} - V_{th(UVLO)}$        | 8    | 9                    | 10   | V             |
| $I_{CC(startup)}$                           | start-up supply current                      | $V_{CC} < V_{startup}$              | 5    | 10                   | 15   | $\mu\text{A}$ |
| $I_{CC(oper)}$                              | operating supply current                     | no load on pin DRIVER               | 0.45 | 0.55                 | 0.65 | mA            |
| $V_{rst(latch)}$                            | latched reset voltage                        |                                     | 4    | 5                    | 6    | V             |
| <b>Input voltage sensing (pin VINSENSE)</b> |  |                                     |      |                      |      |               |
| $V_{start(VINSENSE)}$                       | start voltage on pin VINSENSE                | detection level                     | 0.89 | 0.94                 | 0.99 | V             |
| $V_{det(L)(VINSENSE)}$                      | LOW-level detection voltage on pin VINSENSE  |                                     | 0.68 | 0.72                 | 0.76 | V             |
| $V_{det(H)(VINSENSE)}$                      | HIGH-level detection voltage on pin VINSENSE |                                     | 3.39 | 3.52                 | 3.65 | V             |
| $I_{O(VINSENSE)}$                           | output current on pin VINSENSE               |                                     | -    | -20                  | -    | nA            |
| $V_{clamp(VINSENSE)}$                       | clamp voltage on pin VINSENSE                | $I_{I(VINSENSE)} = 50\ \mu\text{A}$ | -    | 5.2                  | -    | V             |
| <b>Protection input (pin PROTECT)</b>       |  |                                     |      |                      |      |               |
| $V_{det(L)(PROTECT)}$                       | LOW-level detection voltage on pin PROTECT   |                                     | 0.47 | 0.50                 | 0.53 | V             |
| $V_{det(H)(PROTECT)}$                       | HIGH-level detection voltage on pin PROTECT  |                                     | 0.75 | 0.8                  | 0.85 | V             |

**Table 5. Characteristics ...continued**

$T_{amb} = 25\text{ °C}$ ;  $V_{CC} = 20\text{ V}$ ; all voltages are measured with respect to ground (pin 2); currents are positive when flowing into the IC; unless otherwise specified.

| Symbol                                    | Parameter                                    | Conditions   | Min     | Typ     | Max     | Unit              |
|---|--|--|---------|---------|---------|-------------------|
| $I_{O(PROTECT)}$                          | output current on pin PROTECT                | $V_{PROTECT} = V_{low(PROTECT)}$   | -34     | -32     | -30     | $\mu\text{A}$     |
|   |  | $V_{PROTECT} = V_{high(PROTECT)}$  | 87      | 107     | 127     | $\mu\text{A}$     |
| $V_{clamp(PROTECT)}$                      | clamp voltage on pin PROTECT                 | $I_{I(PROTECT)} = 200\ \mu\text{A}$  | [1] 3.5 | 4.1     | 4.7     | V                 |
| <b>Peak current control (pin CTRL)</b>    |  |  |         |         |         |                   |
| $V_{CTRL}$                                | voltage on pin CTRL                          | for minimum flyback peak current   | 1.5     | 1.8     | 2.1     | V                 |
|   |  | for maximum flyback peak current   | 3.4     | 3.9     | 4.3     | V                 |
| $R_{int(CTRL)}$                           | internal resistance on pin CTRL              |  | 5       | 7       | 9       | k $\Omega$        |
| $I_{O(CTRL)}$                             | output current on pin CTRL                   | $V_{CTRL} = 1.4\text{ V}$  | -0.7    | -0.5    | -0.3    | mA                |
|   |  | $V_{CTRL} = 3.7\text{ V}$  | -0.28   | -0.2    | -0.12   | mA                |
| <b>Pulse width modulator</b>              |  |  |         |         |         |                   |
| $f_{osc}$                                 | oscillator frequency                         |  | 85.5    | 91.5    | 97.5    | kHz               |
| $f_{mod}$                                 | modulation frequency                         |  | 210     | 280     | 350     | Hz                |
| $\Delta f_{mod}$                          | modulation frequency variation               |  | $\pm 4$ | $\pm 5$ | $\pm 6$ | kHz               |
| $\delta_{max}$                            | maximum duty cycle                           |  | 68.5    | 72      | 79      | %                 |
| $V_{start(red)f}$                         | frequency reduction start voltage            | pin CTRL   | 1.5     | 1.8     | 2.1     | V                 |
| $V_{\delta(zero)}$                        | zero duty cycle voltage                      | pin CTRL   | 1.25    | 1.55    | 1.85    | V                 |
| <b>Overpower protection (pin OPTIMER)</b> |  |  |         |         |         |                   |
| $V_{prot(OPTIMER)}$                       | protection voltage on pin OPTIMER            |  | 2.4     | 2.5     | 2.6     | V                 |
| $I_{prot(OPTIMER)}$                       | protection current on pin OPTIMER            | no overpower situation   | 100     | 150     | 200     | $\mu\text{A}$     |
|   |  | overpower situation  | -12.2   | -10.7   | -9.2    | $\mu\text{A}$     |
| <b>Restart timer (pin OPTIMER)</b>        |  |  |         |         |         |                   |
| $V_{restart(OPTIMER)}$                    | restart voltage on pin OPTIMER               | low level  | 0.8     | 1.2     | 1.6     | V                 |
|   |  | high level   | 4.1     | 4.5     | 4.9     | V                 |
| $I_{restart(OPTIMER)}$                    | restart current on pin OPTIMER               | charging OPTIMER capacitor   | -127    | -107    | -87     | $\mu\text{A}$     |
|   |  | discharging OPTIMER capacitor  | -0.1    | 0       | 0.1     | $\mu\text{A}$     |
| <b>Current sense (pin ISENSE)</b>         |  |  |         |         |         |                   |
| $V_{sense(max)}$                          | maximum sense voltage                        | $\Delta V/\Delta t = 50\text{ mV}/\mu\text{s}$ ;<br>$V_{VISENSE} = 0.78\text{ V}$  | 0.48    | 0.51    | 0.54    | V                 |
|   |  | $\Delta V/\Delta t = 200\text{ mV}/\mu\text{s}$ ;<br>$V_{VISENSE} = 0.78\text{ V}$ | 0.50    | 0.53    | 0.56    | V                 |
| $V_{th(sense)opp}$                        | overpower protection sense threshold voltage |  | 370     | 400     | 430     | mV                |
| $\Delta V_{ISENSE}/\Delta t$              | slope compensation voltage on pin ISENSE     | $\Delta V/\Delta t = 50\text{ mV}/\mu\text{s}$                                     | 20      | 33      | 46      | mV/ $\mu\text{s}$ |

**Table 5. Characteristics ...continued**

$T_{amb} = 25\text{ }^{\circ}\text{C}$ ;  $V_{CC} = 20\text{ V}$ ; all voltages are measured with respect to ground (pin 2); currents are positive when flowing into the IC; unless otherwise specified.

| Symbol  | Parameter                                    | Conditions  | Min  | Typ              | Max   | Unit               |
|---|--|---|------|------------------|-------|--------------------|
| $t_{leb}$   | leading edge blanking time                   |   | 250  | 300              | 350   | ns                 |
| <b>Overpower compensation (pin VINSENSE and pin ISENSE)</b> |  |   |      |                  |       |                    |
| $I_{opc(ISENSE)}$   | overpower compensation current on pin ISENSE | $V_{VINSENSE} = 1\text{ V}$ ;<br>$V_{sense(max)} > 400\text{ mV}$ | -    | 0.28             | -     | $\mu\text{A}$      |
|   |  | $V_{VINSENSE} = 3\text{ V}$ ;<br>$V_{sense(max)} > 400\text{ mV}$ | -    | 1.7              | -     | $\mu\text{A}$      |
| <b>Soft start (pin ISENSE)</b>                              |  |   |      |                  |       |                    |
| $I_{start(soft)}$   | soft start current                           |   | -63  | -55              | -47   | $\mu\text{A}$      |
| $V_{start(soft)}$   | soft start voltage                           | $V_{CTRL} = 4\text{ V}$ ; enable voltage                          | -    | $V_{sense(max)}$ | -     | V                  |
| $R_{start(soft)}$   | soft start resistance                        |   | 12   | -                | -     | $\text{k}\Omega$   |
| <b>Driver (pin DRIVER)</b>                                  |  |   |      |                  |       |                    |
| $I_{source(DRIVER)}$  | source current on pin DRIVER                 | $V_{DRIVER} = 2\text{ V}$   | -    | -0.3             | -0.25 | A                  |
| $I_{sink(DRIVER)}$  | sink current on pin DRIVER                   | $V_{DRIVER} = 2\text{ V}$   | 0.25 | 0.3              | -     | A                  |
|   |  | $V_{DRIVER} = 10\text{ V}$  | 0.6  | 0.75             | -     | A                  |
| $V_{O(DRIVER)max}$  | maximum output voltage on pin DRIVER         |   | 9    | 10.5             | 12    | V                  |
| <b>Temperature protection</b>                               |  |   |      |                  |       |                    |
| $T_{pl(IC)}$  | IC protection level temperature              |   | 130  | 140              | 150   | $^{\circ}\text{C}$ |

[1] The clamp voltage on the PROTECT pin is lowered when the IC is in power-down (latched or restart protection).

## 11. Application information

A power supply with the TEA1733AT is a flyback converter operating in Continuous conduction mode (see [Figure 10](#)).

Capacitor C5 buffers the IC supply voltage, which is powered via resistor R3 at start-up and via the auxiliary winding during normal operation. Sense resistor R9 converts the current through the MOSFET S1 into a voltage on pin ISENSE. The value of R9 defines the maximum primary peak current on MOSFET S1. Resistor R7 reduces the peak current to capacitor C5.

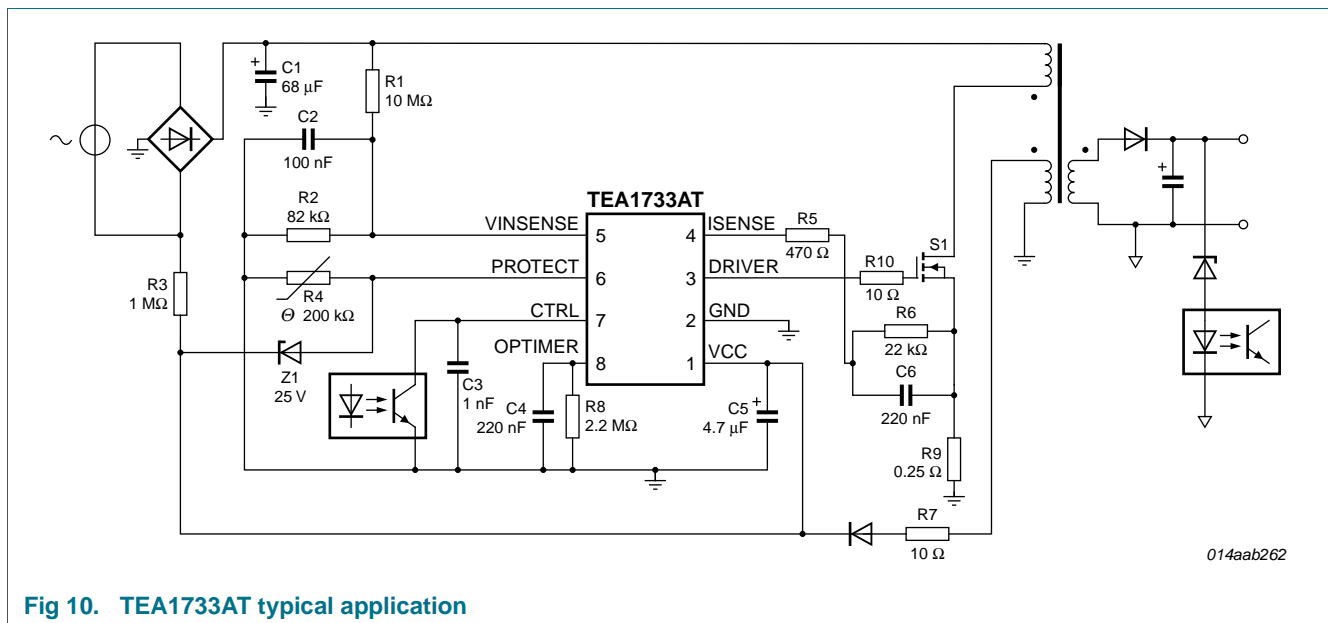
In the example shown in [Figure 10](#), the PROTECT pin is used for OVP and OTP. The OVP level is set by diode Z1 to  $V_{CC} = 25.8$  V. The OTP level is set by Negative Temperature Coefficient (NTC) resistor R4. The VINSENSE pin is used for mains voltage detection and resistors R1 and R2 set the start voltage to about 80 V (AC). The overpower protection time is defined by capacitor C4 at 60 ms.

The restart time is defined by capacitor C4 and resistor R8 at 0.5 s.

Resistor R6 and capacitor C6 define the soft start time. Resistor R5 prevents the soft start capacitor C6 from being charged during normal operation caused by negative voltage spikes across the current sense resistor R9.

Capacitor C3 reduces noise on the CTRL pin.

See the application note for more information ([Ref. 1](#)).



**Fig 10. TEA1733AT typical application**

014aab262

## 12. Package outline

SO8: plastic small outline package; 8 leads; body width 3.9 mm

SOT96-1

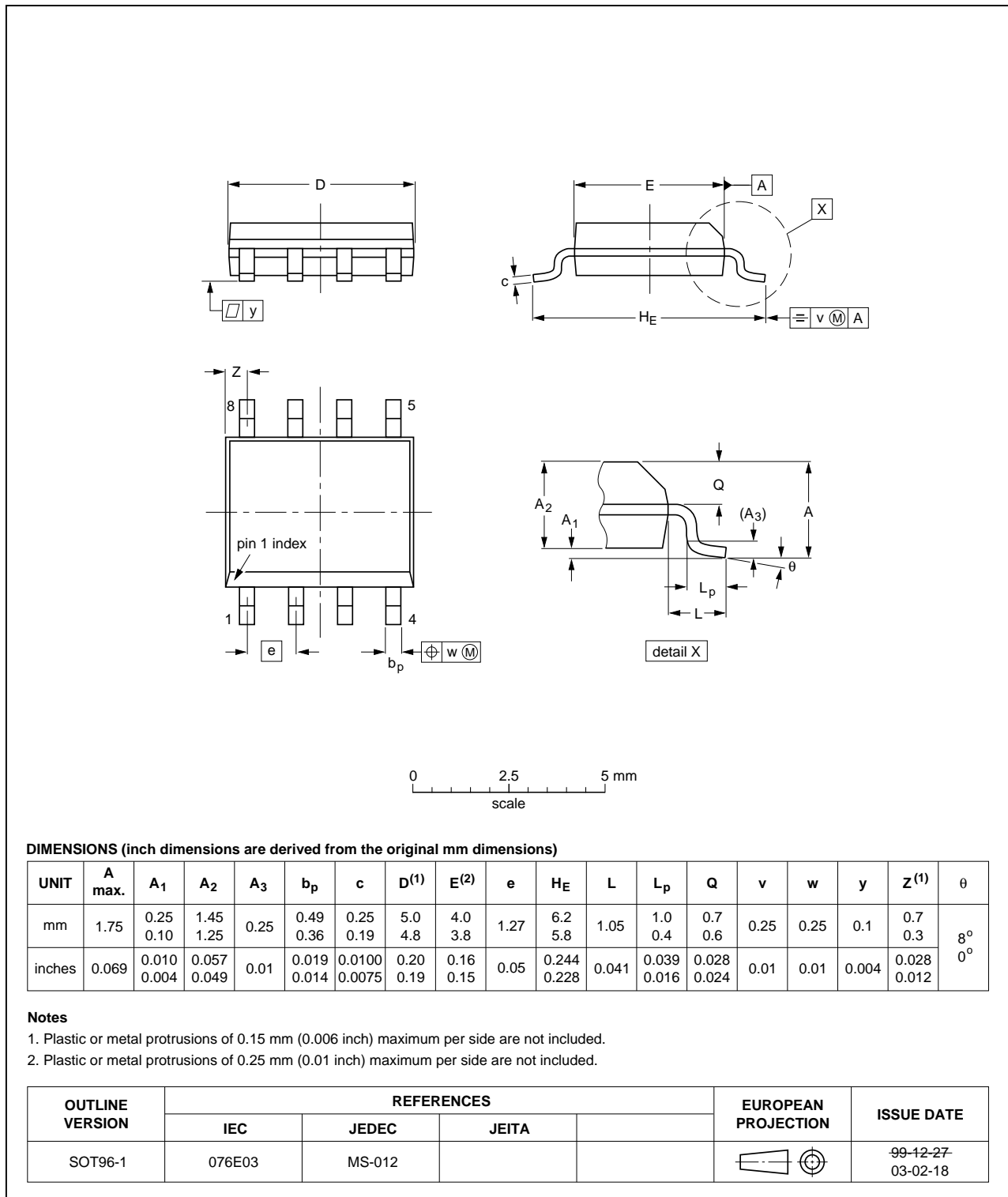


Fig 11. Package outline SOT96-1 (SO8)

## 13. References

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- [1] **AN10868** — GreenChip TEA1733 series fixed frequency flyback controller

## 14. Revision history

Table 6. Revision history

| Document ID    | Release date | Data sheet status   | Change notice | Supersedes    |
|----------------|--------------|---|---------------|---------------|
| TEA1733AT v.3  | 20130524     | Product data sheet  | -             | TEA1733AT v.2 |
| Modifications: |              | <ul style="list-style-type: none"><li>• Data sheet status changed from Preliminary to Product.</li><li>• <a href="#">Section 11 "Application information"</a> has been updated.</li><li>• <a href="#">Section 13 "References"</a> has been added.</li></ul> |               |               |
| TEA1733AT v.2  | 20101119     | Preliminary data sheet  | -             | TEA1733AT v.1 |
| TEA1733AT v.1  | 20100716     | Preliminary data sheet  | -             | -             |



## 15. Legal information

### 15.1 Data sheet status

| Document status <sup>[1][2]</sup> | Product status <sup>[3]</sup> | Definition  |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet      | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet    | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet        | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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Date of release: 24 May 2013  
 Document identifier: TEA1733AT